

具有 1.5A 高侧电流源的 LM36011 无感应器、单 LED 闪光灯驱动器

1 特性

- 精确且可编程 LED 电流
 - 闪光灯/IR 电流范围：11mA 至 1.5A（128 个级别）
 - 手电筒电流范围：2.4mA 至 376mA（128 个级别）
- 闪光灯超时时间长达 1.6 秒
- 优化了低电池电量条件下的闪光灯 LED 电流 (IVFM)
- LED 阴极对地短路运行以提高热管理
- 较小的总体解决方案尺寸：< 4mm²
- 硬件闪光灯使能 (STROBE)
- 输入电压范围：2.5V 至 5.5V
- 400kHz I²C 兼容接口
 - I²C 地址 = 0x64

2 应用

- 手机
- 平板电脑
- IR LED 驱动器
- 视频监控：IP 摄像机
- 条形码扫描仪
- 便携式数据终端

3 说明

LM36011 是一款超小型 LED 闪光灯驱动器，具有高度可调节性。总解决方案尺寸为 4mm²，可提供高达 1.5A 的 LED 闪光灯电流或高达 376mA 的手电筒电流。以上所有的操作均可通过精确的可编程电流源来完成，无需预调节电压，从而减小解决方案尺寸并节省成本。

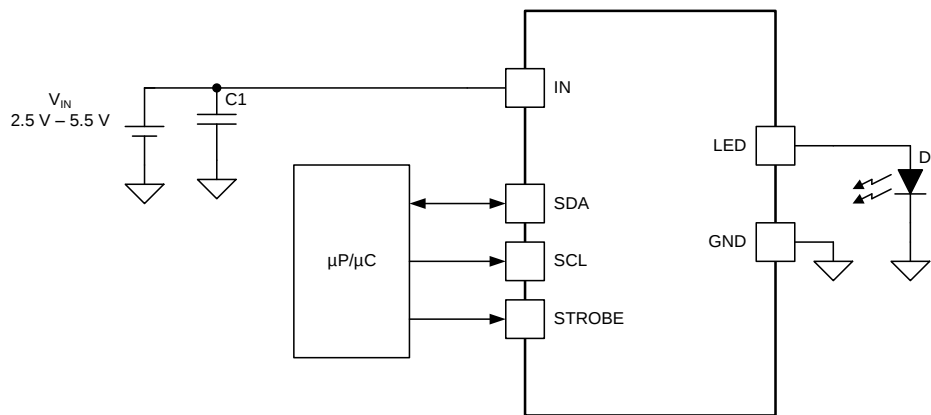
LM36011 的功能由与 I²C 兼容的接口控制。这些特性包括：硬件闪光灯 (STROBE)、闪光灯超时、UVLO、热比例缩减、LED 故障检测以及适用于闪光灯和影片模式（手电筒）的 128 级可编程电流。该器件可在 -40°C 至 +85°C 的环境温度范围下工作。

器件信息⁽¹⁾

器件型号	封装	封装尺寸（标称值）
LM36011	DSBGA (8)	1.512mm × 0.800mm

(1) 要了解所有可用封装，请参阅数据表末尾的可订购产品附录。

简化电路原理图



Copyright © 2016, Texas Instruments Incorporated



目录

1	特性	1	7.4	Device Functioning Modes.....	11
2	应用	1	7.5	Programming.....	13
3	说明	1	7.6	Register Descriptions	15
4	修订历史记录	2	8	Applications and Implementation	17
5	Pin Configuration and Functions	3	8.1	Application Information.....	17
6	Specifications	4	8.2	Typical Application	17
6.1	Absolute Maximum Ratings	4	9	Power Supply Recommendations	23
6.2	ESD Ratings.....	4	10	Layout	23
6.3	Recommended Operating Conditions.....	4	10.1	Layout Guidelines	23
6.4	Thermal Information	4	10.2	Layout Example	23
6.5	Electrical Characteristics.....	5	11	器件和文档支持	24
6.6	Timing Requirements	5	11.1	器件支持	24
6.7	Typical Characteristics	6	11.2	文档支持	24
7	Detailed Description	9	11.3	商标	24
7.1	Overview	9	11.4	静电放电警告	24
7.2	Functional Block Diagram	9	11.5	Glossary	24
7.3	Feature Description	10	12	机械、封装和可订购信息	24

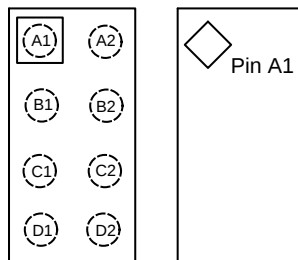
4 修订历史记录

注：之前版本的页码可能与当前版本有所不同。

日期	修订版本	注意
2017 年 7 月	*	最初发布版本

5 Pin Configuration and Functions

**YKB Package
8-Pin DSBGA
Top View**



Pin Functions

PIN		TYPE ⁽¹⁾	DESCRIPTION
NAME	NO.		
A1	GND	G	Ground
A2,C1	IN	P	Input voltage connection. Connect IN to the input supply and bypass to GND with a 10-μF or larger ceramic capacitor.
B2	STROBE	I	Active high hardware flash enable. Drive STROBE high to turn on flash pulse. An internal pulldown resistor of 300 kΩ is between STROBE and GND.
C2	SDA	I/O	I ² C serial data input/output.
D1	LED	P	High-side current source output for flash LED.
D2	SCL	I	I ² C serial clock input.
B1	No Connect	—	No connect

(1) G = Ground; P = Power; I = Input; O = Output

6 Specifications

6.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)⁽¹⁾⁽²⁾

	MIN	MAX	UNIT
IN, OUT, LED	−0.3	6	V
SDA, SCL, STROBE	−0.3	(V _{IN} + 0.3) w/ 6 V maximum	
Continuous power dissipation ⁽³⁾	Internally limited		
Junction temperature, T _{J-MAX}		150	°C
Storage temperature, T _{stg}	−65	150	°C

- (1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions*. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) All voltages are with respect to the potential at the GND pin.
- (3) Internal thermal shutdown circuitry protects the device from permanent damage. Thermal shutdown engages at T_J = 150°C (typical) and disengages at T_J = 135°C (typical). Thermal shutdown is ensured by design.

6.2 ESD Ratings

		VALUE	UNIT
V _(ESD) Electrostatic discharge	Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001 ⁽¹⁾	±1000	V
	Charged-device model (CDM), per JEDEC specification JESD22-C101 ⁽²⁾	±250	

- (1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
- (2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

6.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)⁽¹⁾⁽²⁾

	MIN	MAX	UNIT
V _{IN}	2.5	5.5	V
Junction temperature, T _J	−40	125	°C
Ambient temperature, T _A ⁽³⁾	−40	85	°C

- (1) Stresses beyond those listed under *absolute maximum ratings* may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *recommended operating conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) All voltages are with respect to the potential at the GND pin.
- (3) In applications where high power dissipation and/or poor package thermal resistance is present, the maximum ambient temperature may have to be derated. Maximum ambient temperature (T_{A-MAX}) is dependent on the maximum operating junction temperature (T_{J-MAX-OP} = 125°C), the maximum power dissipation of the device in the application (P_{D-MAX}), and the junction-to-ambient thermal resistance of the part/package in the application (R_{θJA}), as given by the following equation: T_{A-MAX} = T_{J-MAX-OP} − (R_{θJA} × P_{D-MAX}).

6.4 Thermal Information

THERMAL METRIC ⁽¹⁾		LM36011	UNIT
		YKB (DSBGA)	
		8 PINS	
R _{θJA}	Junction-to-ambient thermal resistance	117.3	°C/W
R _{θJC(top)}	Junction-to-case (top) thermal resistance	1.3	°C/W
R _{θJB}	Junction-to-board thermal resistance	34.3	°C/W
Ψ _{JT}	Junction-to-top characterization parameter	0.5	°C/W
Ψ _{JB}	Junction-to-board characterization parameter	34.6	°C/W

- (1) For more information about traditional and new thermal metrics, see the *Semiconductor and IC Package Thermal Metrics* application report.

6.5 Electrical Characteristics

$T_A = 25^\circ\text{C}$ and $V_{IN} = 3.6\text{ V}$, unless otherwise specified. Minimum and maximum limits apply over the full operating ambient temperature range ($-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$).⁽¹⁾⁽²⁾

PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT
CURRENT SOURCE SPECIFICATIONS							
I _{LED}	Current source accuracy	V _{IN} = 4 V, flash code = 0x7F = 1.5 A ⁽³⁾		-10%	1.5	10%	A
		V _{IN} = 4 V, torch code = 0x7F = 376 mA		-10%	376	10%	mA
V _{HR}	LED current source regulation voltage	I _{LED} = 1.5 A	Flash	550		mV	
		I _{LED} = 376 mA	Torch	350			
STEP-UP DC-DC CONVERTER SPECIFICATIONS							
V _{UVLO}	Undervoltage lockout threshold	Falling V _{IN}		2.5		V	
V _{IVFM}	Input voltage flash monitor trip threshold	Reg 0x02, bits [7:5] = 000		-3%	2.9	3%	V
I _Q	Quiescent supply current			0.3		mA	
I _{SB}	Standby supply current	Device disabled 2.5 V ≤ V _{IN} ≤ 5.5 V		0.8		4	μA
STROBE VOLTAGE SPECIFICATIONS							
V _{IL}	Input logic low	2.5 V ≤ V _{IN} ≤ 5.5 V		0	0.4		V
V _{IH}	Input logic high			1.2	V _{IN}		V
I ² C-COMPATIBLE INTERFACE SPECIFICATIONS (SCL, SDA)							
V _{IL}	Input logic low	2.5 V ≤ V _{IN} ≤ 4.2 V		0	0.4		V
V _{IH}	Input logic high			1.2	V _{IN}		
V _{OL}	Output logic low	I _{LOAD} = 3 mA		400		mV	

- (1) Minimum (MIN) and Maximum (MAX) limits are specified by design, test, or statistical analysis. Typical (TYP) numbers are not verified, but do represent the most likely norm. Unless otherwise specified, conditions for typical specifications are: $V_{IN} = 3.6\text{ V}$ and $T_A = 25^\circ\text{C}$.
- (2) All voltages are with respect to the potential at the GND pin.
- (3) The ability to deliver 1.5 A of LED current is highly dependent upon the input voltage, LED voltage, ambient temperature and PCB layout. Depending upon the system conditions, it is possible that the device could hit the internal thermal shutdown or thermal scale-back value before the desired flash duration is reached. See [Thermal Performance](#) for more details.

6.6 Timing Requirements

		MIN	NOM	MAX	UNIT
t_1	SCL clock period	2.4			μs
t_2	Data in set-up time to SCL high	100			ns
t_3	Data out stable after SCL low	0			ns
t_4	SDA low set-up time to SCL low (start)	100			ns
t_5	SDA high hold time after SCL high (stop)	100			ns

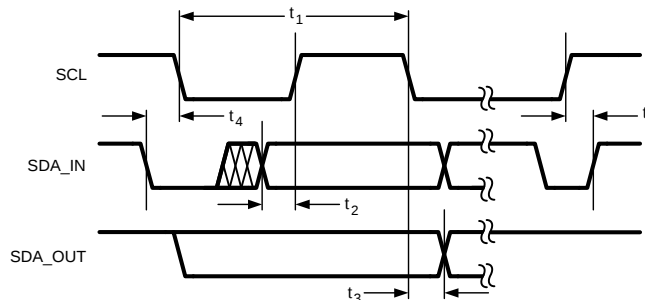


图 1. I²C-Compatible Interface Specifications

6.7 Typical Characteristics

$T_A = 25^\circ\text{C}$, $V_{IN} = 3.6\text{ V}$, $C_{IN} = 10\text{ }\mu\text{F}$, $V_{LED} = 3.4\text{ V}$, Flash Time-out = 320 ms and Thermal Scale-Back (TSB) disabled, unless otherwise noted.

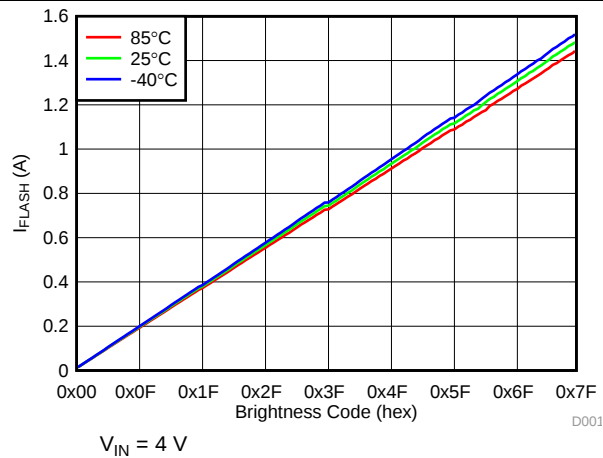


图 2. LED Flash Current vs Brightness Code

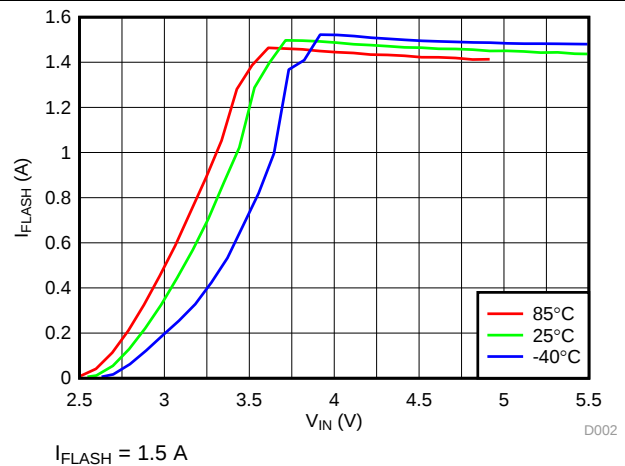


图 3. LED Flash Current vs Input Voltage

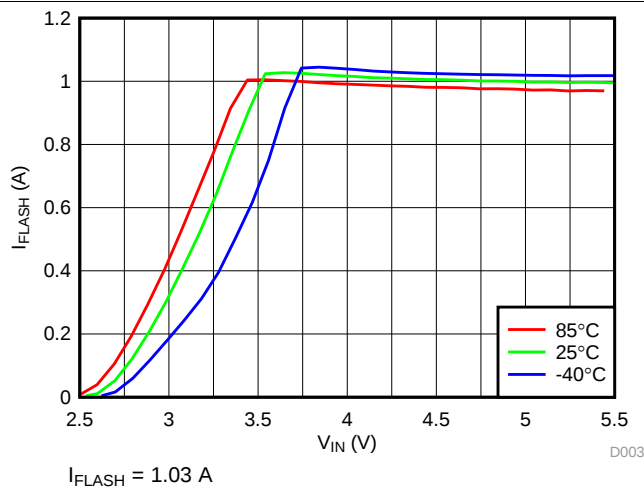


图 4. LED Flash Current vs Input Voltage

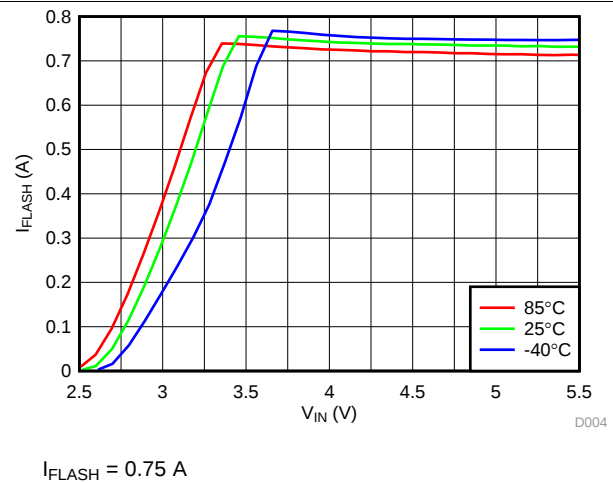


图 5. LED Flash Current vs Input Voltage

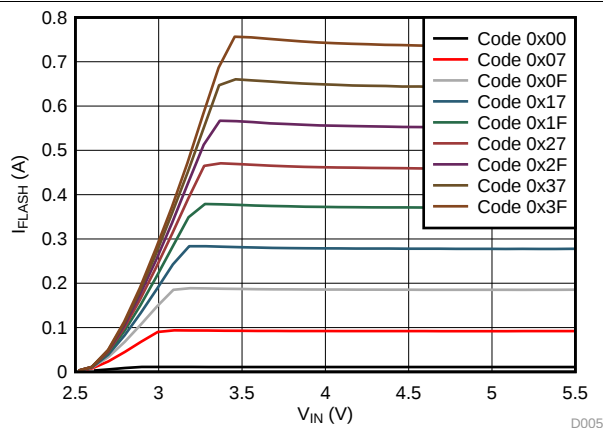


图 6. LED Flash Current vs Input Voltage

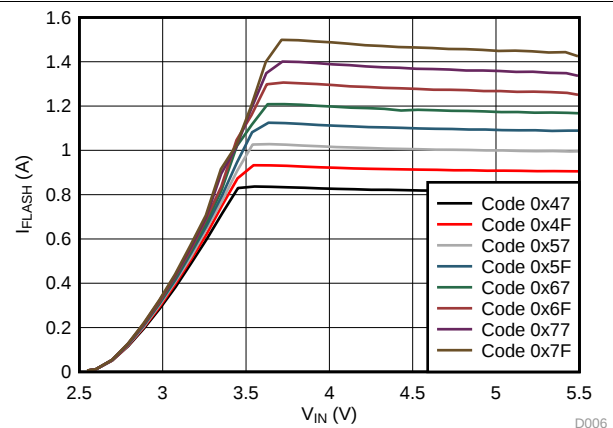


图 7. LED Flash Current vs Input Voltage

Typical Characteristics (接下页)

$T_A = 25^\circ\text{C}$, $V_{IN} = 3.6\text{ V}$, $C_{IN} = 10\text{ }\mu\text{F}$, $V_{LED} = 3.4\text{ V}$, Flash Time-out = 320 ms and Thermal Scale-Back (TSB) disabled, unless otherwise noted.

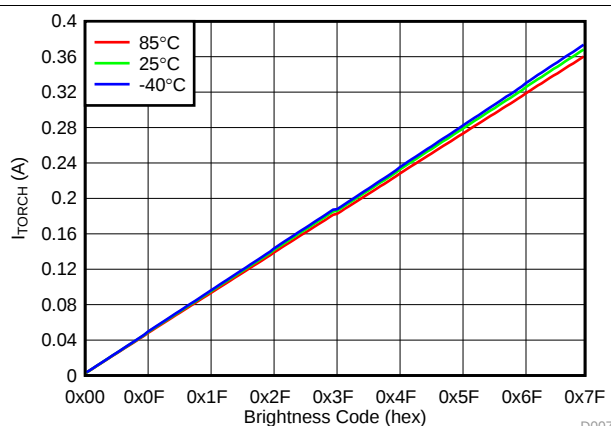


图 8. LED Torch Current vs Brightness Code

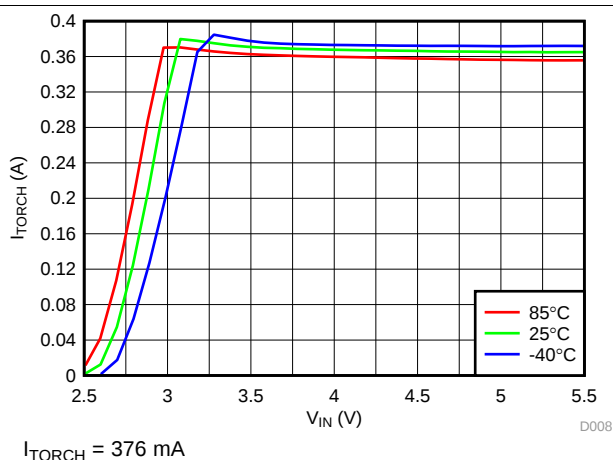


图 9. LED Torch Current vs Input Voltage

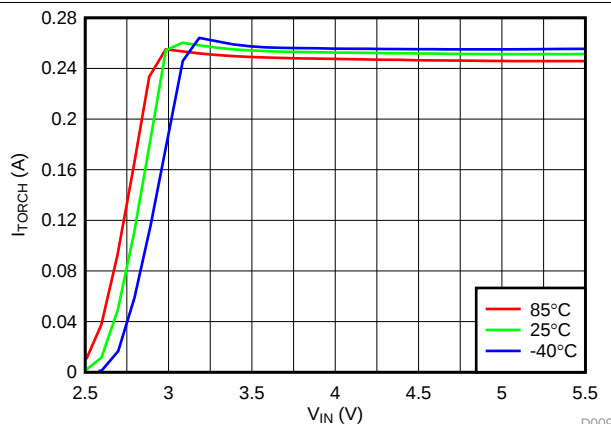


图 10. LED Torch Current vs Input Voltage

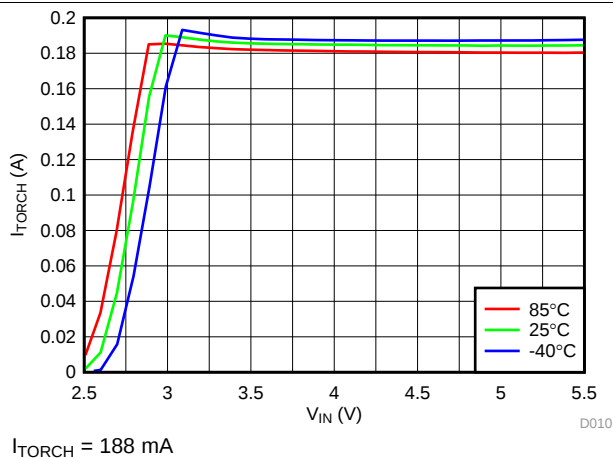


图 11. LED Torch Current vs Input Voltage

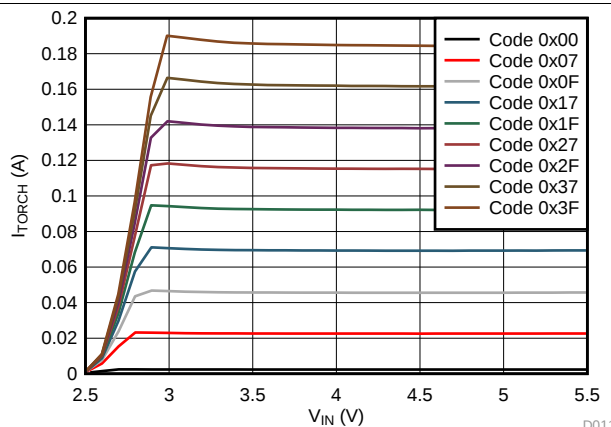


图 12. LED Torch Current vs Input Voltage

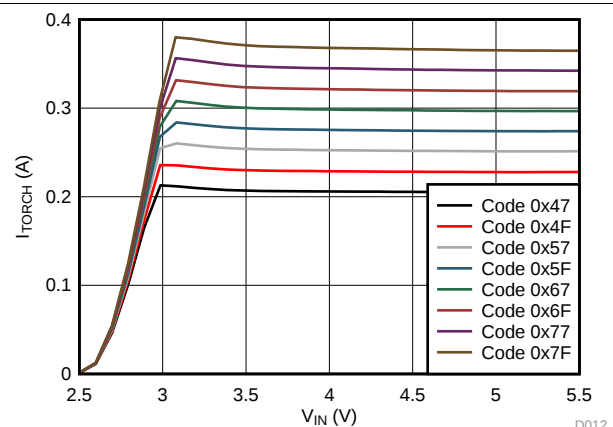


图 13. LED Torch Current vs Input Voltage

Typical Characteristics (接下页)

$T_A = 25^\circ\text{C}$, $V_{IN} = 3.6\text{ V}$, $C_{IN} = 10\text{ }\mu\text{F}$, $V_{LED} = 3.4\text{ V}$, Flash Time-out = 320 ms and Thermal Scale-Back (TSB) disabled, unless otherwise noted.

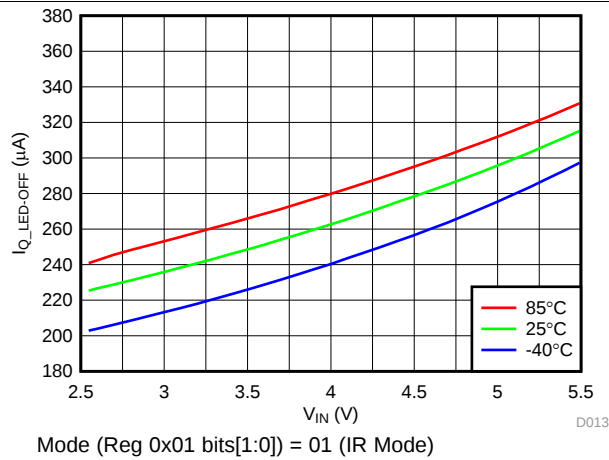


图 14. LED Off Current vs Input Voltage

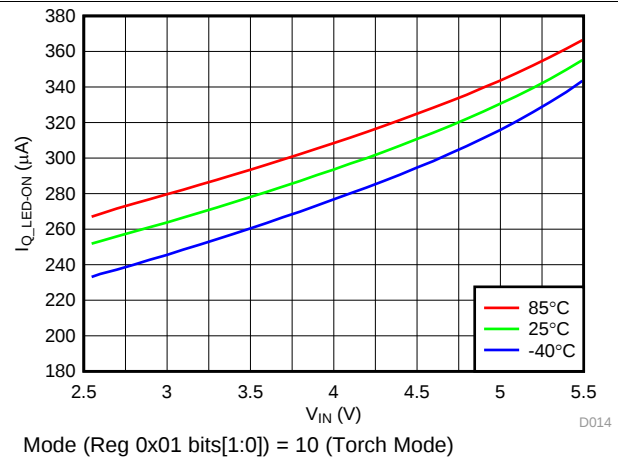


图 15. LED On Current vs Input Voltage

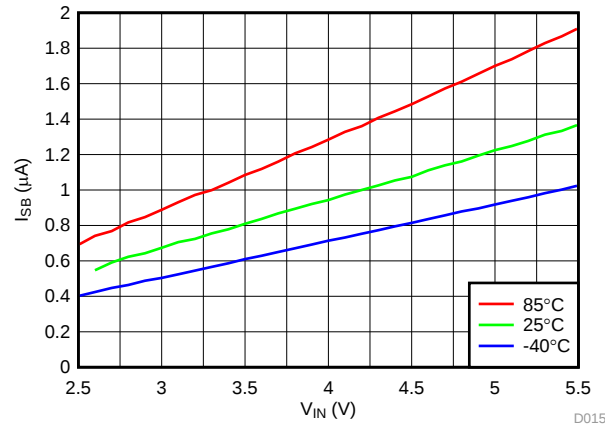


图 16. Standby Current vs Input Voltage

7 Detailed Description

7.1 Overview

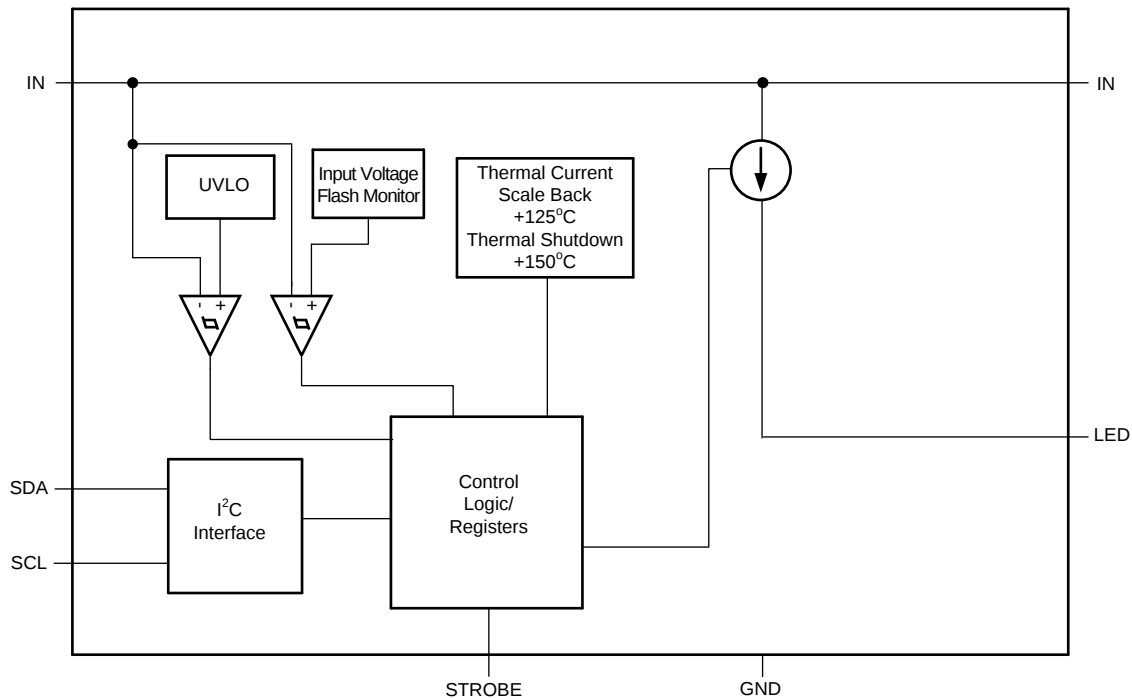
The LM36011 is a high-power white LED flash driver capable of delivering up to 1.5 A to the LED over the 2.5-V to 5.5-V input voltage range.

The device has one logic input for a hardware flash enable (STROBE). This logic input has an internal 300-k Ω (typical) pulldown resistor to GND.

Additional features of the device include an input voltage monitor that can reduce the flash current during low V_{IN} conditions and a temperature based current scale-back feature that forces the flash current to the set torch level if the on-chip junction temperature reaches 125°C.

Control is done via an I²C-compatible interface. This includes adjustment of the flash and torch current levels, and changing the flash time-out duration. Additionally, there are flag and status bits that indicate flash current time-out, LED over-temperature condition, LED failure (open/short), device thermal shutdown, thermal current scale-back, and V_{IN} undervoltage conditions.

7.2 Functional Block Diagram



Copyright © 2017, Texas Instruments Incorporated

7.3 Feature Description

7.3.1 Flash Mode

In flash mode, the LED current source provides 128 target current levels from 11 mA to 1.5 A, set by the LED Flash Brightness Register (0x03 bits [6:0]). Flash mode is activated by the Enable Register (0x01), setting mode M1, M0 (bits [1:0]) to 11. Once the flash sequence is activated, the LED current source ramps up to the programmed flash current by stepping through all current steps until the programmed current is reached.

When flash mode is enabled using the mode M1, M0 (bits [1:0]) of the Enable Register (0x01), the mode bits in the Enable Register are cleared after a flash time-out event.

7.3.2 Torch Mode

In torch mode, the LED current source provides 128 target current levels from 2.4 mA to 376 mA, set by the LED Torch Brightness Register (0x04 bits [6:0]). Torch mode is activated by the Enable Register (0x01), setting mode M1, M0 (bits [1:0]) to 10. Once the TORCH sequence is activated, the LED current source ramps up to the programmed torch current by stepping through all current steps until the programmed current is reached. The rate at which the current ramps is determined by the value chosen in the Torch Ramp bit [0] in Timing Register (0x02).

7.3.3 IR Mode

In IR mode, the target LED current is equal to the value stored in the LED Flash Brightness Register (0x03 bits [7:0]). IR mode is enabled by the Enable Register (0x01) setting mode M1, M0 (bits [1:0]) to 01. In IR mode, toggling the STROBE pin enables and disables the LED current source. The STROBE pin can only be set to be Level sensitive, as all timing of the IR pulse is externally controlled. In IR mode, the current source does not control the ramp rate of the LED output. The current transitions immediately from off to on and then on to off.

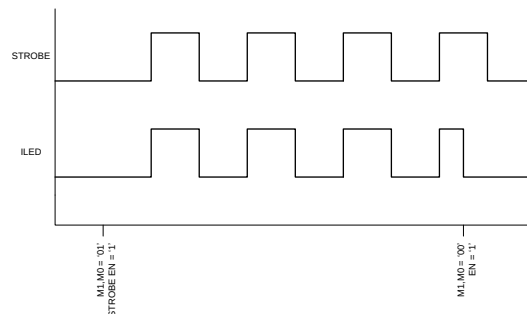
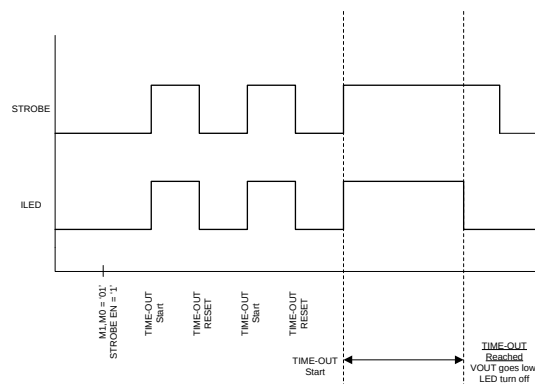


图 17. IR Mode



- (1) When the flash timer elapses, the device goes into stand-by regardless of strobe state

图 18. IR Mode Time-out

7.4 Device Functioning Modes

7.4.1 Start-Up (Enabling The Device)

At turnon the LED current source steps through each FLASH or TORCH level until the target LED current is reached. This gives the device a controlled turnon and limits inrush current from the V_{IN} supply. The target LED flash and the target LED torch currents are set by the LED Flash Brightness Register (0x03 bits [6:0]) and LED Torch Brightness Register (0x04 bits [6:0]) respectively.

7.4.2 Input Voltage Flash Monitor (IVFM)

The LM36011 has the ability to adjust the flash current based upon the voltage level present at the IN pin utilizing the input voltage flash monitor (IVFM). The adjustable threshold IVFM-D ranges from 2.9 V to 3.6 V in 100-mV steps and is set by Configuration Register (0x02) bits [7:5]. Additionally, the IVFM-D threshold sets the input voltage boundary that forces the LM36011 to stop ramping the flash current during start-up.

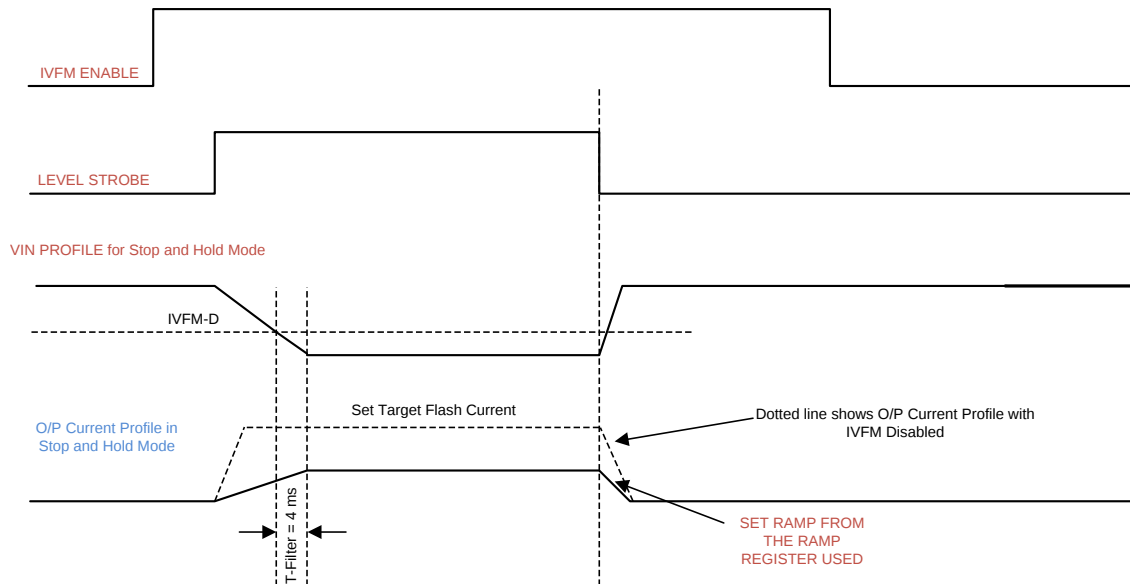


图 19. IVFM Mode

7.4.3 Fault/Protections

Upon a fault condition, the LM36011 sets the appropriate flag(s) in the Flags Register (0x05) and switches into stand-by mode obtained by clearing the mode M1, M0 (bits [1:0]) of the Enable Register (0x01). The LM36011 remains in standby until an I²C read of the Flags Register. I²C read of the Flags Register clears the flags and the fault status can be re-checked. If the fault(s) is still present, the LM36011 re-sets the appropriate flag bits and enters stand-by again.

7.4.3.1 Input Voltage Flash Monitor (IVFM)

When the input voltage crosses the IVFM-D value, programmed by Configuration Register (0x02) bits [7:5], the LM36011 sets the IVFM flag (bit [6]) of Flags Register (0x05).

7.4.3.2 LED Short Fault

LM36011 enters stand-by mode from flash or torch mode and V_{LED} Short Fault flag (bit [5]) of Flags Register (0x05) is set, if the LED output experiences a short condition. An LED short condition occurs if the voltage at the LED pin goes below 500 mV (typical). There is a deglitch time of 256 μ s before the LED short flag is valid. The LED short fault can be reset to 0 by removing power to the LM36011, or setting the software reset field (Register 0x06 bit [7]) to a 1, or by reading back the Flags Register.

Device Functioning Modes (接下页)

7.4.3.3 Thermal Scale-Back (TSB)

When the LM36011 die temperature reaches 125°C, the thermal scale-back (TSB) circuit trips and TSB flag (bit [2]) of Flags Register (0x05) is set. The LED current then shifts to torch current level, set by the LED Torch Brightness Register (0x04 bits [6:0]) for the duration of the flash pulse, set by the flash time-out in the Configuration Register (0x02 bits [4:1]). After I²C read of the Flags Register and upon re-flash, if the die temperature is still above 125°C, the LM36011 re-enters into torch current level and sets the TSB flag bit again.

7.4.3.4 Thermal Shutdown (TSD)

When the LM36011 die temperature reaches 150°C, the thermal shutdown (TSD) circuit trips, forcing the LM36011 into standby and writing a 1 to the TSD flag (bit [2]) of the Flags Register (0x05). The LM36011 restarts only after the Flags Register is read, which clears the fault flag. Upon restart, if the die temperature is still above 150°C, the LM36011 resets the TSD flag and re-enters standby.

7.4.3.5 Undervoltage Lockout (UVLO)

The LM36011 has an internal comparator that monitors the voltage at IN pin. If the input voltage drops to 2.5 V, the UVLO flag (bit [1]) of Flags Register (0x05) is set and the LM36011 switches to stand-by mode. After the UVLO flag is set, even if the input voltage rises above 2.5 V, the LM36011 is not available for operation until there is an I²C read of the Flags Register. Upon an I²C read of the Flags Register, the UVLO fault is cleared and normal operation can resume.

7.4.3.6 Flash Time-out (FTO)

The LM36011 sources the flash current for the time period set by Flash Time-out (0x02 bits [4:1]). The LED current source has 16 time-out levels ranging from 40 ms to 1600 ms.

7.5 Programming

7.5.1 Control Truth Table

M1 (Register 0x01 bit[1])	M0 (Register 0x01 bit[0])	STROBE EN (Register 0x01 bit[2])	STROBE PIN	ACTION
0	0	0	X	Standby
0	0	1	pos edge	Ext flash
1	0	X	X	Int torch
1	1	X	X	Int flash
0	1	0	X	IR LED standby
0	1	1	0	IR LED standby
0	1	1	pos edge	IR LED enabled

7.5.2 I²C-Compatible Interface

7.5.2.1 Data Validity

The data on SDA must be stable during the HIGH period of the clock signal (SCL). In other words, the state of the data line can only be changed when SCL is LOW.

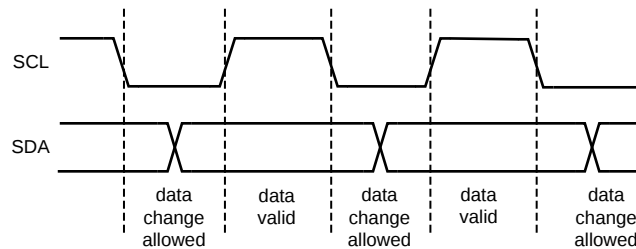


图 20. Data Validity Data

A pullup resistor between the VIO line of the controller and SDA must be greater than $[(VIO - V_{OL}) / 3 \text{ mA}]$ to meet the V_{OL} requirement on SDA. Using a larger pullup resistor results in lower switching current with slower edges, while using a smaller pullup resistor results in higher switching currents with faster edges.

7.5.2.2 Start and Stop Conditions

START and STOP conditions classify the beginning and the end of the I²C session. A START condition is defined as the SDA signal transitioning from HIGH to LOW while SCL line is HIGH. A STOP condition is defined as the SDA transitioning from LOW to HIGH while SCL is HIGH. The I²C master always generates START and STOP conditions. The I²C bus is considered busy after a START condition and free after a STOP condition. During data transmission, the I²C master can generate repeated START conditions. First START and repeated START conditions are equivalent, function-wise.

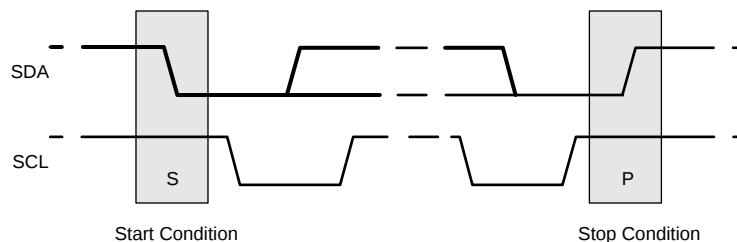
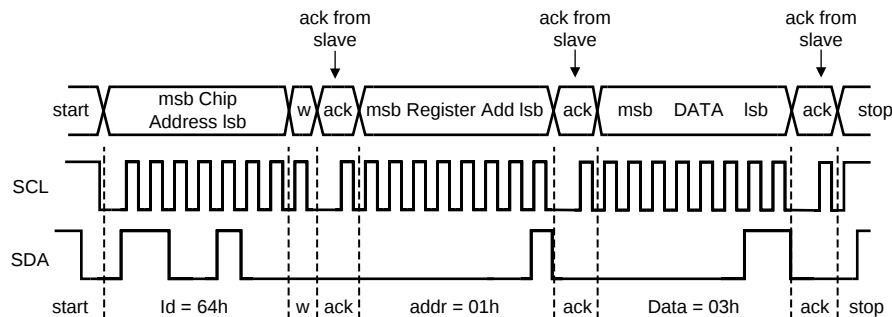


图 21. Start and Stop Conditions

7.5.2.3 Transferring Data

Every byte of data put on the SDA line must be eight bits long, with the most significant bit (MSB) transferred first. Each byte of data has to be followed by an acknowledge bit. The acknowledge related clock pulse is generated by the master. The master releases the SDA line (HIGH) during the acknowledge clock pulse. The LM36011 pulls down the SDA line during the 9th clock pulse, signifying an acknowledge. The LM36011 generates an acknowledge after each byte is received. There is no acknowledge created after data is read from the device.

After the START condition, the I²C master sends a chip address. This address is seven bits long followed by an eighth bit which is a data direction bit (R/W). The LM36011 7-bit address is 0x64. For the eighth bit, a 0 indicates a WRITE, and a 1 indicates a READ. The second byte selects the register to which the data is written. The third byte contains data to write to the selected register.



**图 22. Write Cycle W = Write (SDA = 0) R = Read (SDA = 1) Ack = Acknowledge
(SDA Pulled Down by Either Master or Slave) ID = Chip Address, 64h for LM36011**

7.5.2.4 I²C-Compatible Chip Address

The device address for the LM36011 is 1100100 (0x64). After the START condition, the I²C-compatible master sends the 7-bit address followed by an eighth read or write bit (R/W). R/W = 0 indicates a WRITE and R/W = 1 indicates a READ. The second byte following the device address selects the register address to which the data is written. The third byte contains the data for the selected register.

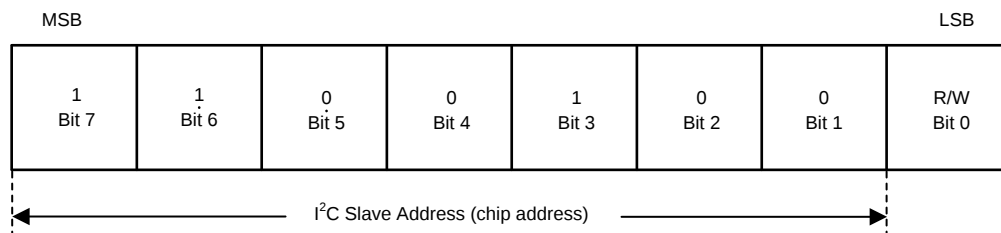


图 23. I²C-Compatible Chip Address

7.6 Register Descriptions

REGISTER NAME	INTERNAL HEX ADDRESS	POWER ON/RESET VALUE
		LM36011
Enable Register	0x01	0x20
Configuration Register	0x02	0x15
LED Flash Brightness Register	0x03	0x00
LED Torch Brightness Register	0x04	0x00
Flags Register	0x05	0x00
Device ID Register	0x06	0x01

7.6.1 Enable Register (0x01)

Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
RFU	RFU	RFU	IVFM Enable 0 = Disabled (Default) 1 = Enabled	Strobe Type 0 = Level Triggered (Default) 1 = Edge Triggered	Strobe Enable 0 = Disabled (Default) 1 = Enabled	Mode Bits: M1, M0 00 = Standby (Default) 01 = IR Drive 10 = Torch 11 = Flash	

注

Edge strobe mode is not valid in IR MODE. Switching between level and edge strobe types while the device is enabled is not recommended.

In edge or level strobe mode, TI recommends that the trigger pulse width be set greater than 1 ms to ensure proper turn-on of the device.

7.6.2 Configuration Register (0x02)

Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
IVFM Levels (IVFM-D) 000 = 2.9 V (Default) 001 = 3 V 010 = 3.1 V 011 = 3.2 V 100 = 3.3 V 101 = 3.4 V 110 = 3.5 V 111 = 3.6 V			Flash Time-out Duration 0000 = 40 ms 0001 = 80 ms 0010 = 120 ms 0011 = 160 ms 0100 = 200 ms 0101 = 240 ms 0110 = 280 ms 0111 = 320 ms 1000 = 360 ms 1001 = 400 ms 1010 = 600 ms (Default) 1011 = 800 ms 1100 = 1000 ms 1101 = 1200 ms 1110 = 1400 ms 1111 = 1600 ms				Torch Ramp 0 = No Ramp 1 = 1 ms (default)

注

On the LM36011, special care must be taken with regards to thermal management when using time-out values greater than 500 ms. Depending on the PCB layout, input voltage, and output current, it is possible to have the internal thermal shutdown circuit trip prior to reaching the desired flash time-out value.

7.6.3 LED Flash Brightness Register (0x03)

Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Thermal Current Scale-Back 0 = Disabled 1 = Enabled (default) If enabled, the LED current shifts to torch current level if T_J reaches 125 °C	LED Flash Brightness Level 0000000 = 11 mA (Default) 00010101 (0x15) = 0.257 A 0111111 (0x3F) = 0.75 A 0101111 (0x5F) = 1.03 A 01100110 (0x66) = 1.2 A 1111111 (0x7F) = 1.5 A						

7.6.4 LED Torch Brightness Register (0x04)

Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
RFU	LED Torch Brightness Levels 0000000 = 2.4 mA (Default) 00010101 (0x15) = 64 mA 0111111 (0x3F) = 188 mA 0101111 (0x5F) = 258 mA 01100110 (0x66) = 302 mA 1111111 (0x7F) = 376 mA						

7.6.5 Flags Register (0x05)

Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
RFU	IVFM Trip Flag	V_{LED} Short Fault	RFU	Thermal Current Scale-back (TSB) Flag	Thermal Shutdown (TSD) Fault	UVLO Fault	Flash Time-Out Flag

7.6.6 Device ID and RESET Register (0x06)

Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Software RESET 0 = Normal (default) 1 = Force device RESET	RFU	Device ID 000			Silicon Revision Bits 001		

8 Applications and Implementation

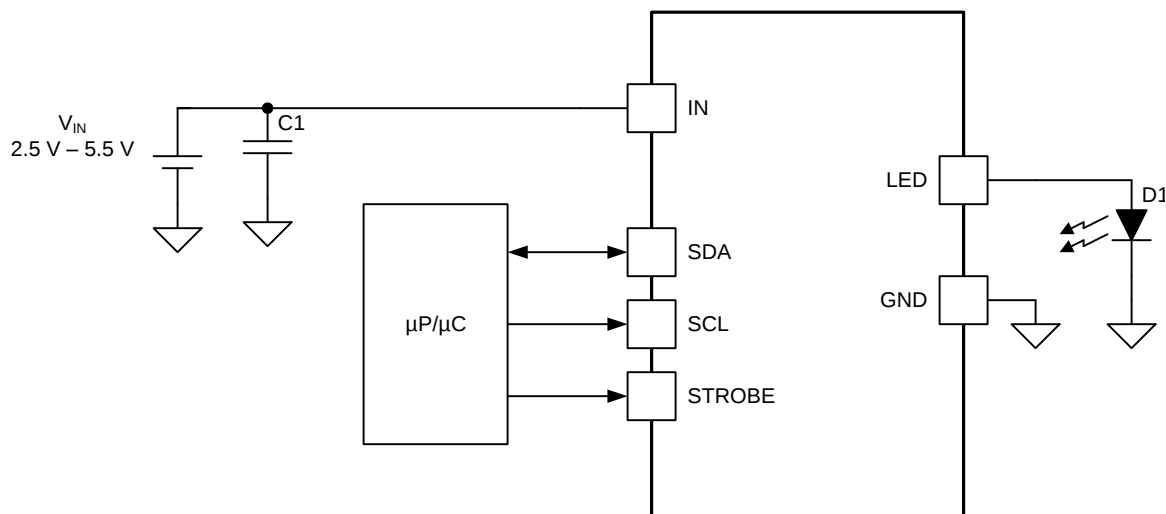
注

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

8.1 Application Information

The LM36011 can drive a flash LED at currents up to 1.5 A. .

8.2 Typical Application



Copyright © 2016, Texas Instruments Incorporated

图 24. LM36011 Typical Application

8.2.1 Design Requirements

Example requirements based on default register values:

表 1. Design Parameters

DESIGN PARAMETER	EXAMPLE VALUE
Input voltage range	2.5 V to 5.5 V
Brightness control	I ² C Register
LED configuration	1 flash LED
Flash brightness	1.5-A maximum current

8.2.2 Detailed Design Procedure

8.2.2.1 Thermal Performance

Output power is limited by two things: the ambient temperature, and the maximum power dissipation in the package. If the die temperature of the device is below the absolute maximum rating of 125°C, the maximum output power can be over 6 W. However, any appreciable output current causes the internal power dissipation to increase and therefore increase the die temperature. Any circuit configuration must ensure that the die temperature remains below 125°C taking into account the ambient temperature derating. The thermal scale-back protection (TSB) helps ensure that temperature requirement is held valid. If the TSB feature is disabled, thermal shutdown (TSD) is the next level of protection for the device, which is set to 150°C. This mechanism cannot be disabled, and operation of the device above 125°C is not ensured by the electrical specification.

The device power dissipation equals:

$$P_{DISS} = (V_{IN} - V_{LED}) \times I_{LED} \quad (1)$$

Use 公式 2 to calculate the junction temperature (T_J) of the device:

$$T_J = R_{\theta JA} \times P_{DISS} \quad (2)$$

Note that these equations only provide approximation of the junction temperature and do not take into account thermal time constants, which play a large role in determining maximum deliverable output power and flash durations.

8.2.2.2 Input Capacitor Selection

Choosing the correct size and type of input capacitor helps minimize noise on the input pin of the boost converter that can feed through and disrupt internal analog signals. In the typical application circuit a 10-μF ceramic input capacitor works well. It is important to place the input capacitor as close as possible to the LM36011 input (IN) pin. This reduces the series resistance and inductance that can inject noise into the device. 表 2 lists various input capacitors recommended for use with the LM36011.

表 2. Recommended InputCapacitors (X5R/X7R Dielectric)

MANUFACTURER	PART NUMBER	VALUE	CASE SIZE	VOLTAGE RATING
TDK Corporation	C1608JB0J106M	10 μF	0603 (1.6 mm × 0.8 mm × 0.8 mm)	6.3 V
TDK Corporation	C2012JB1A106M	10 μF	0805 (2 mm × 1.25 mm × 1.25 mm)	10 V
Murata	GRM188R60J106M	10 μF	0603 (1.6 mm × 0.8 mm × 0.8 mm)	6.3 V
Murata	GRM21BR61A106KE19	10 μF	0805 (2 mm × 1.25 mm × 1.25 mm)	10 V

8.2.3 Application Curves

$T_A = 25^\circ\text{C}$, $V_{IN} = 3.6\text{ V}$, $C_{IN} = 10\text{ }\mu\text{F}$, $V_{LED} = 3.4\text{ V}$, Flash Time-out = 320 ms and Thermal Scale-Back (TSB) disabled, unless otherwise noted.

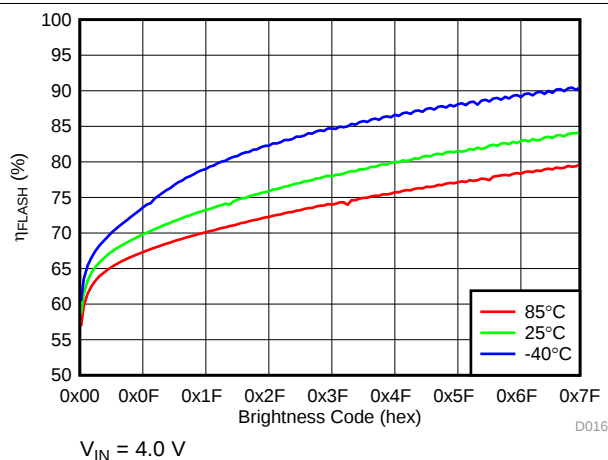


图 25. LED Flash Efficiency vs Brightness Code

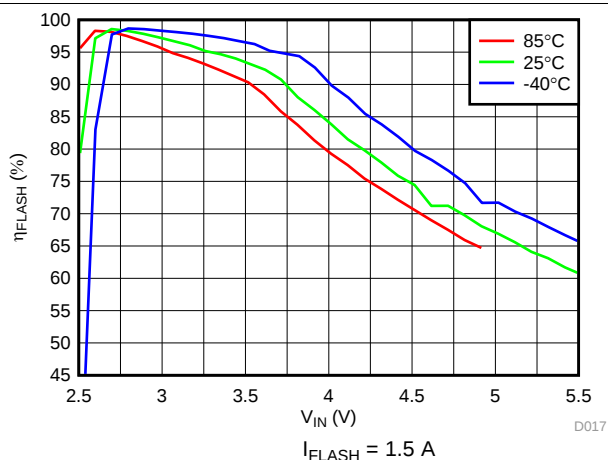


图 26. LED Flash Efficiency vs Input Voltage

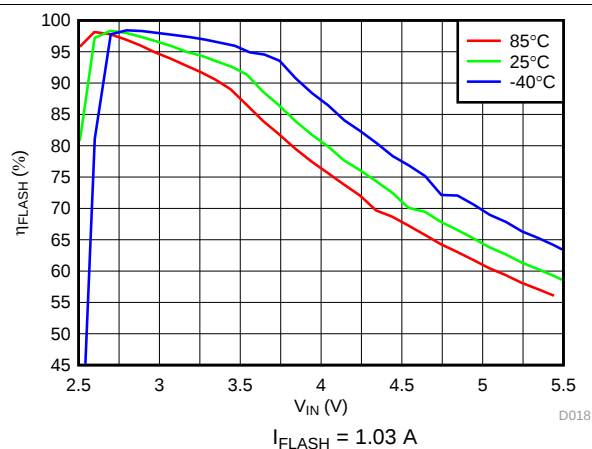


图 27. LED Flash Efficiency vs Input Voltage

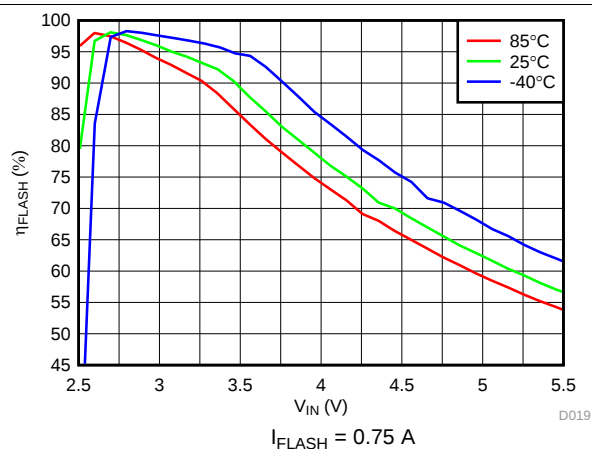


图 28. LED Flash Efficiency vs Input Voltage

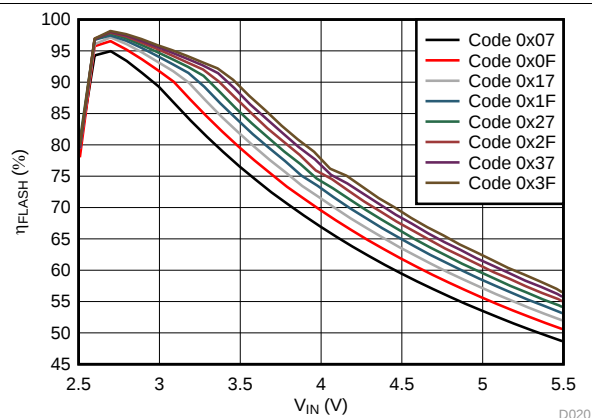


图 29. LED Flash Efficiency vs Input Voltage

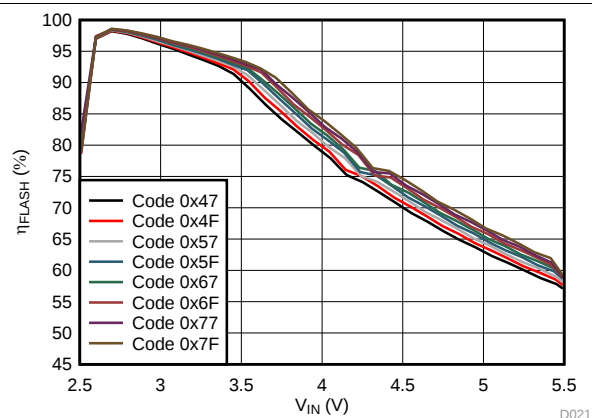


图 30. LED Flash Efficiency vs Input Voltage

LM36011

ZHCSGJ7 – JULY 2017

www.ti.com.cn

$T_A = 25^\circ\text{C}$, $V_{IN} = 3.6\text{ V}$, $C_{IN} = 10\text{ }\mu\text{F}$, $V_{LED} = 3.4\text{ V}$, Flash Time-out = 320 ms and Thermal Scale-Back (TSB) disabled, unless otherwise noted.

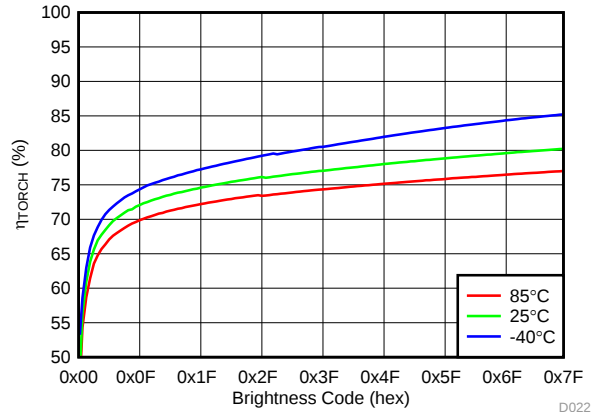


图 31. LED Torch Efficiency vs Brightness Code

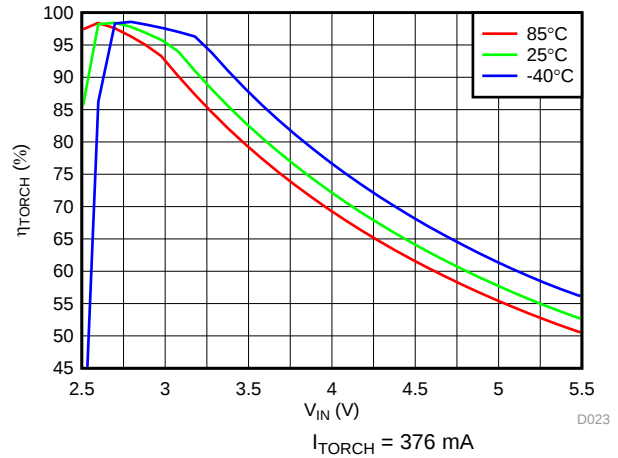


图 32. LED Torch Efficiency vs Input Voltage

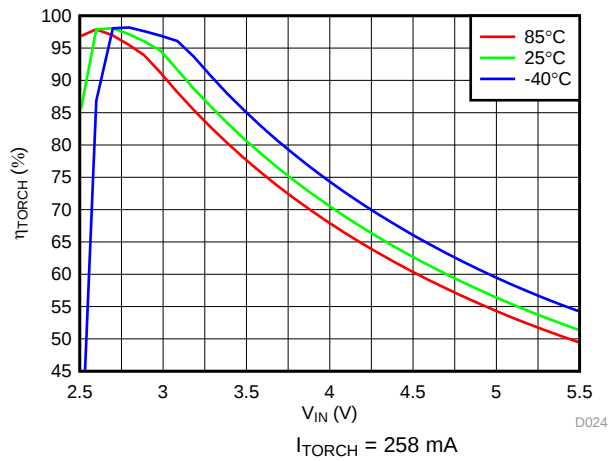


图 33. LED Torch Efficiency vs Input Voltage

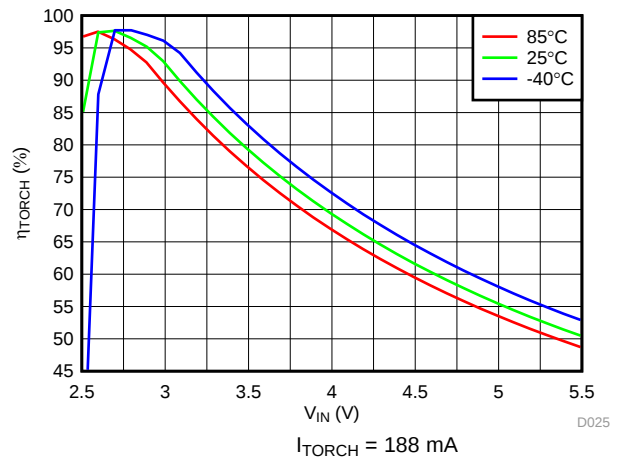


图 34. LED Torch Efficiency vs Input Voltage

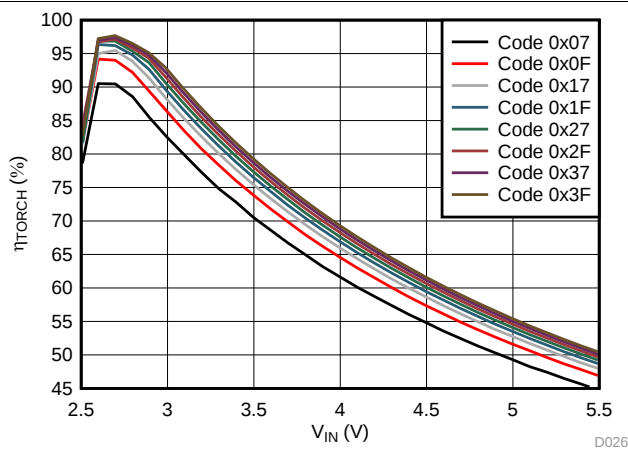


图 35. LED Torch Efficiency vs Input Voltage

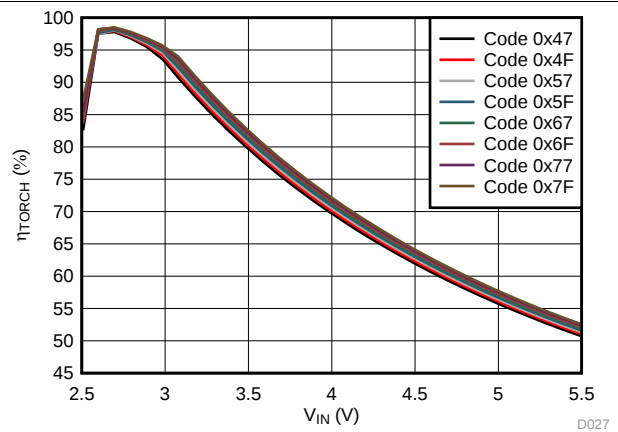
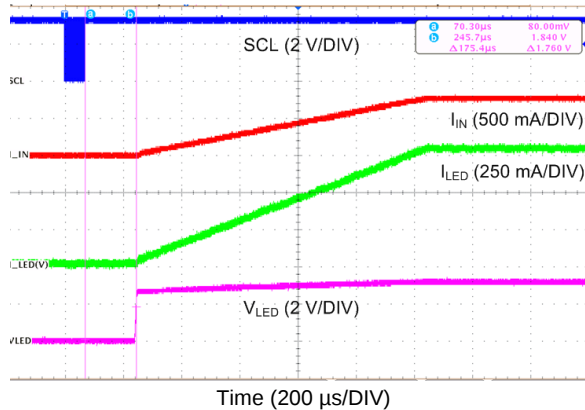


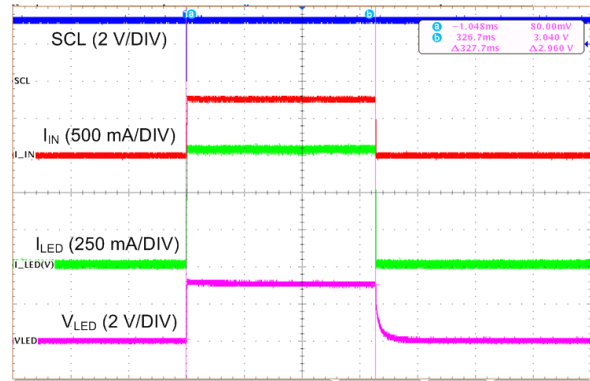
图 36. LED Torch Efficiency vs Input Voltage

$T_A = 25^\circ\text{C}$, $V_{IN} = 3.6\text{ V}$, $C_{IN} = 10\text{ }\mu\text{F}$, $V_{LED} = 3.4\text{ V}$, Flash Time-out = 320 ms and Thermal Scale-Back (TSB) disabled, unless otherwise noted.



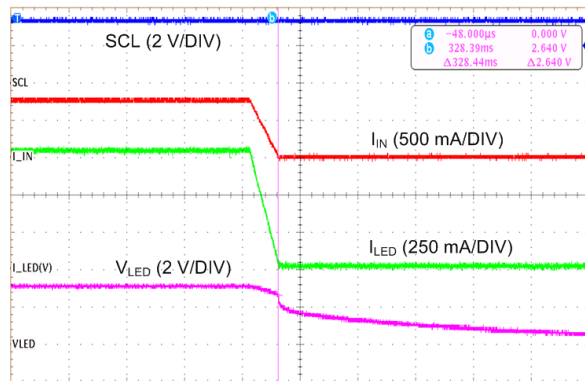
Time (200 μs /DIV)
Mode bits (Reg 0x01 bit[1:0]) = 11 (Flash mode)

图 37. Flash Start-up with I²C



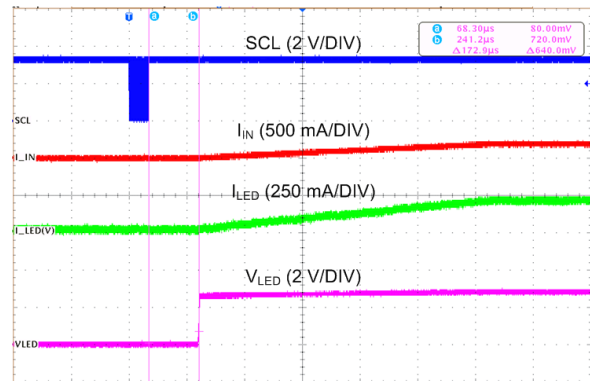
Time (100 ms/DIV)
Flash Time-out (Reg 0x02 bits[4:1]) = 0111 (320 ms)

图 38. Flash Time-out



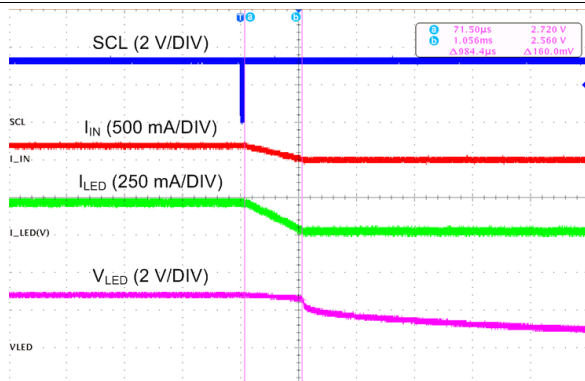
Time (2 ms/DIV)

图 39. Flash Turn-off with I²C



Time (200 μs /DIV)
Mode bits (Reg 0x01 bit[1:0]) = 10 (Torch mode)

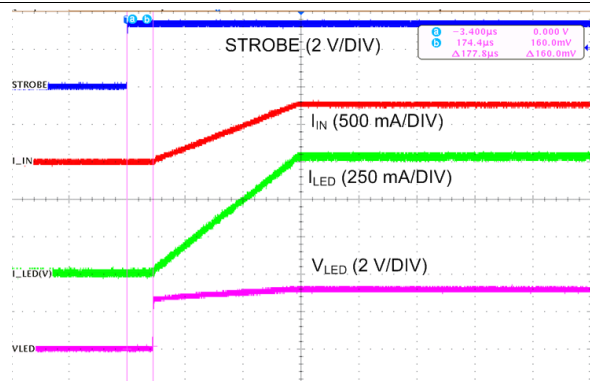
图 40. Torch Start-up with I²C



Time (1 ms/DIV)

Mode bits (Reg 0x01 bit[1:0]) = 00 (Standby mode)

图 41. Torch Turn-off with I²C



Time (400 μs /DIV)

STROBE Enabled (Reg 0x01 bit[2] = 1)

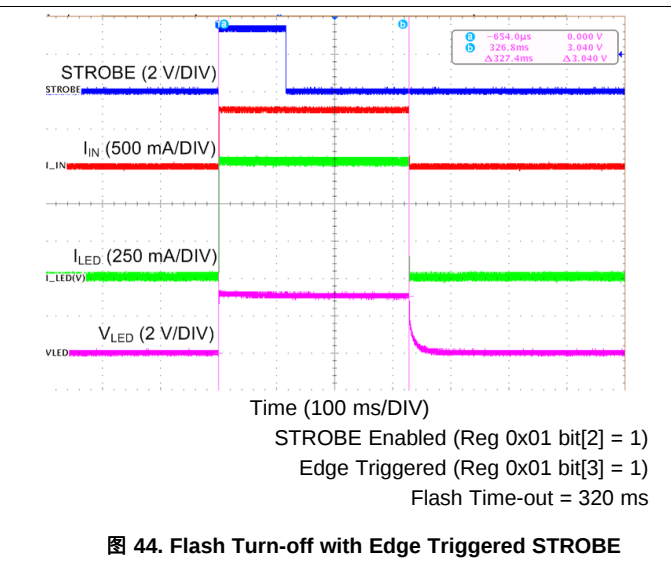
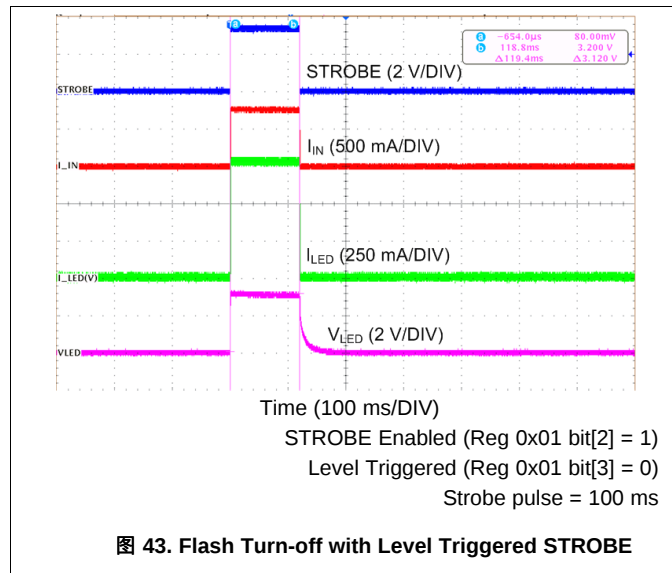
图 42. Flash Start-up with STROBE

LM36011

ZHCSGJ7 – JULY 2017

www.ti.com.cn

$T_A = 25^\circ\text{C}$, $V_{IN} = 3.6\text{ V}$, $C_{IN} = 10\text{ }\mu\text{F}$, $V_{LED} = 3.4\text{ V}$, Flash Time-out = 320 ms and Thermal Scale-Back (TSB) disabled, unless otherwise noted.



9 Power Supply Recommendations

The LM36011 is designed to operate from an input voltage supply range between 2.5 V and 5.5 V. This input supply must be well regulated and capable to supply the required input current. If the input supply is located far from the LM36011 additional bulk capacitance may be required in addition to the ceramic bypass capacitors.

10 Layout

10.1 Layout Guidelines

The following steps are to be used as a reference to ensure the device is stable and maintains proper LED current regulation across its intended operating voltage and current range.

1. Place C_{IN} on the top layer (same layer as the LM36011) and as close as possible to the device. Connecting the input capacitor through short, wide traces to both the IN and GND pins reduces the inductive voltage spikes that occur during switching which can corrupt the V_{IN} line.
2. Terminate the flash LED cathode directly to the GND pin of the LM36011. If possible, route the LED return with a dedicated path so as to keep the high amplitude LED current out of the GND plane. For a flash LED that is routed relatively far away from the LM36011, a good approach is to sandwich the forward and return current paths over the top of each other on two layers. This helps reduce the inductance of the LED current path.

10.2 Layout Example

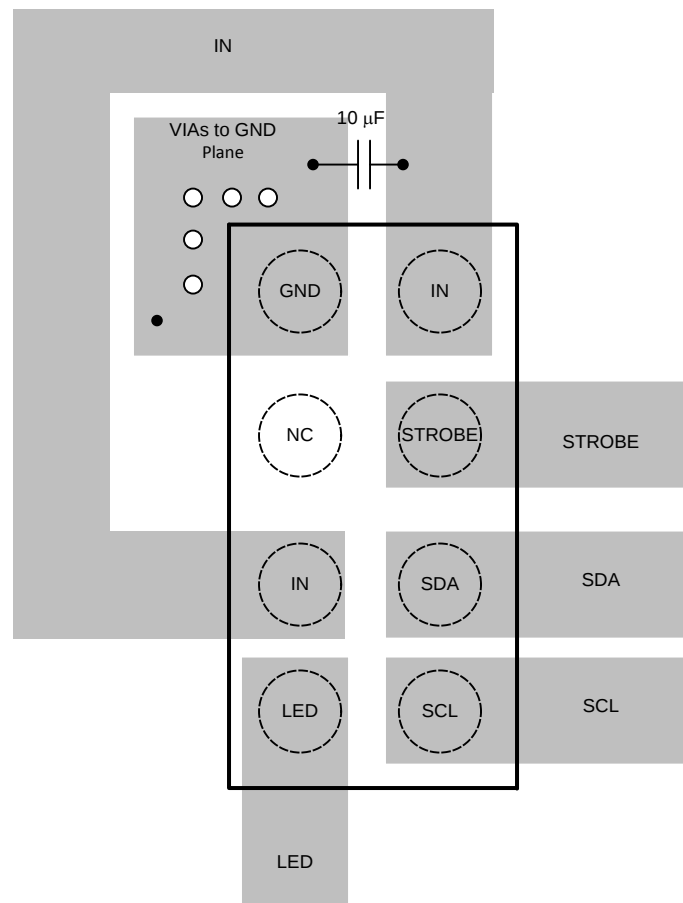


图 45. LM36011 Layout Example

11 器件和文档支持

11.1 器件支持

11.1.1 Third-Party Products Disclaimer

TI'S PUBLICATION OF INFORMATION REGARDING THIRD-PARTY PRODUCTS OR SERVICES DOES NOT CONSTITUTE AN ENDORSEMENT REGARDING THE SUITABILITY OF SUCH PRODUCTS OR SERVICES OR A WARRANTY, REPRESENTATION OR ENDORSEMENT OF SUCH PRODUCTS OR SERVICES, EITHER ALONE OR IN COMBINATION WITH ANY TI PRODUCT OR SERVICE.

11.2 文档支持

11.2.1 相关文档

相关文档如下：

[AN-1112 DSBGA 晶圆级芯片级封装](#)

11.3 商标

All trademarks are the property of their respective owners.

11.4 静电放电警告



ESD 可能会损坏该集成电路。德州仪器 (TI) 建议通过适当的预防措施处理所有集成电路。如果不遵守正确的处理措施和安装程序，可能会损坏集成电路。

ESD 的损坏小至导致微小的性能降级，大至整个器件故障。精密的集成电路可能更容易受到损坏，这是因为非常细微的参数更改都可能会导致器件与其发布的规格不相符。

11.5 Glossary

[SLYZ022](#) — *TI Glossary*.

This glossary lists and explains terms, acronyms, and definitions.

12 机械、封装和可订购信息

以下页面包括机械、封装和可订购信息。这些信息是指定器件的最新可用数据。这些数据发生变化时，我们可能不会另行通知或修订此文档。如欲获取此产品说明书的浏览器版本，请参阅左侧的导航栏。

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LM36011YKBR	ACTIVE	DSBGA	YKB	8	3000	Green (RoHS & no Sb/Br)	SAC396	Level-1-260C-UNLIM	-40 to 85	6011	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "-" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

TAPE AND REEL INFORMATION


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LM36011YKBR	DSBGA	YKB	8	3000	180.0	8.4	0.9	1.61	0.57	2.0	8.0	Q1

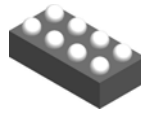
TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LM36011YKBR	DSBGA	YKB	8	3000	182.0	182.0	20.0

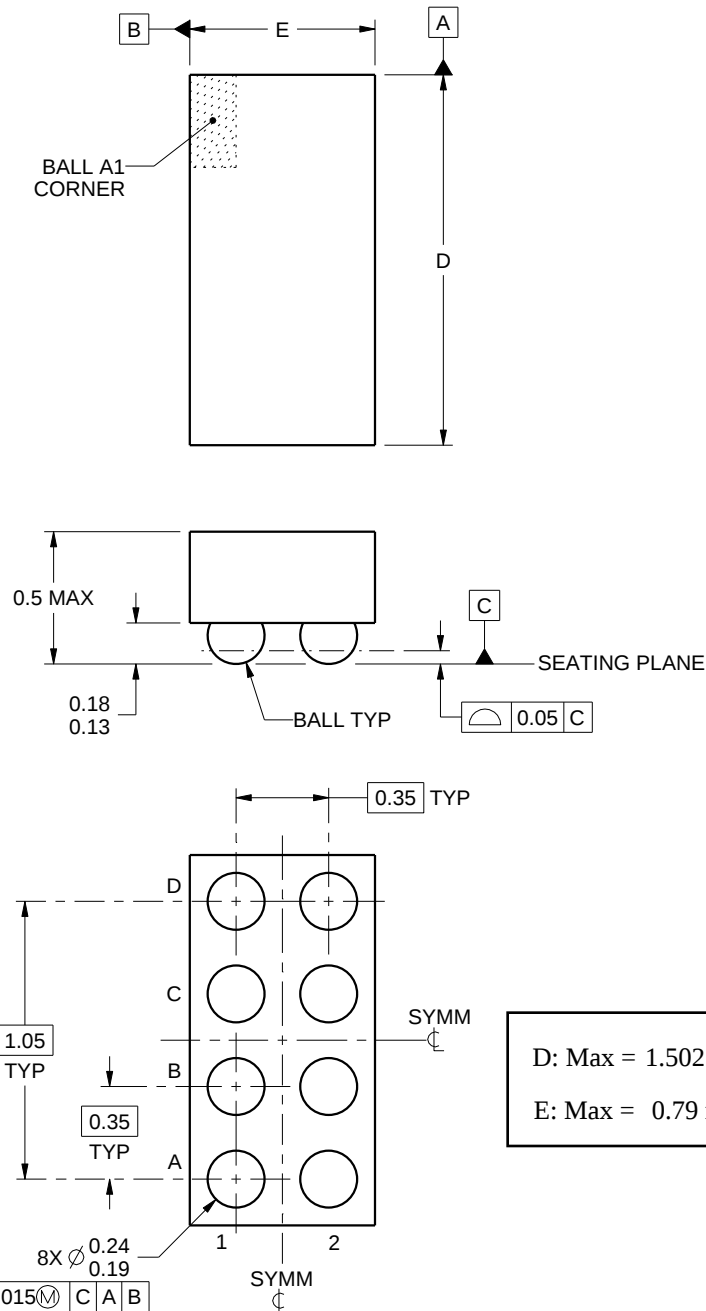
YKB0008



PACKAGE OUTLINE

DSBGA - 0.5 mm max height

DIE SIZE BALL GRID ARRAY



4223222/A 08/2016

NOTES:

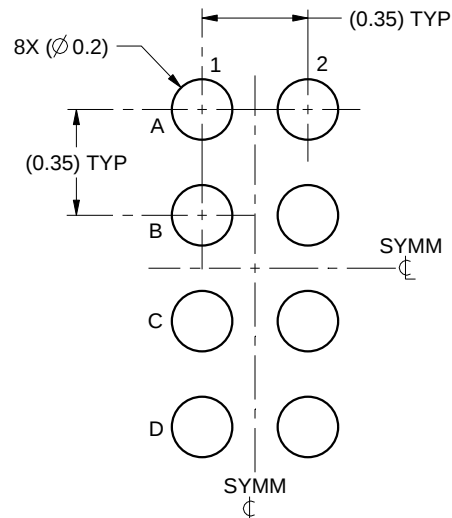
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.

EXAMPLE BOARD LAYOUT

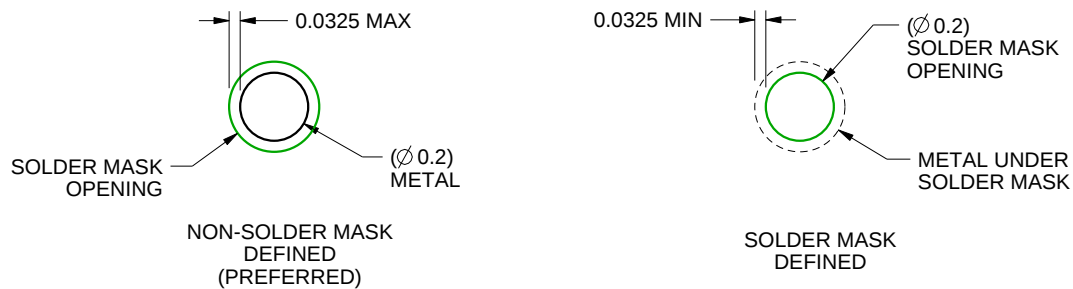
YKB0008

DSBGA - 0.5 mm max height

DIE SIZE BALL GRID ARRAY



LAND PATTERN EXAMPLE
SCALE:40X

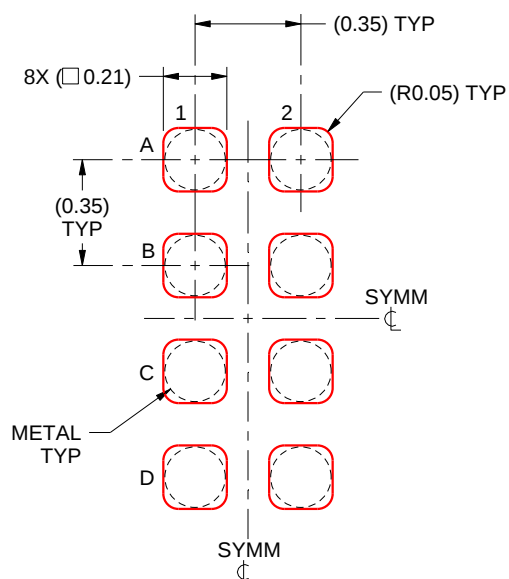


SOLDER MASK DETAILS
NOT TO SCALE

4223222/A 08/2016

NOTES: (continued)

- Final dimensions may vary due to manufacturing tolerance considerations and also routing constraints. For more information, see Texas Instruments literature number SNVA009 (www.ti.com/lit/snva009).



SOLDER PASTE EXAMPLE
 BASED ON 0.075 - 0.1 mm THICK STENCIL
 SCALE:40X

4223222/A 08/2016

NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release.

重要声明

德州仪器 (TI) 公司有权按照最新发布的 JESD46 对其半导体产品和服务进行纠正、增强、改进和其他修改，并不再按最新发布的 JESD48 提供任何产品和服务。买方在下订单前应获取最新的相关信息，并验证这些信息是否完整且是最新的。

TI 公布的半导体产品销售条款 (<http://www.ti.com/sc/docs/stdterms.htm>) 适用于 TI 已认证和批准上市的已封装集成电路产品的销售。另有其他条款可能适用于其他类型 TI 产品及服务的使用或销售。

复制 TI 数据表上 TI 信息的重要部分时，不得变更该等信息，且必须随附所有相关保证、条件、限制和通知，否则不得复制。TI 对该等复制文件不承担任何责任。第三方信息可能受到其它限制条件的制约。在转售 TI 产品或服务时，如果存在对产品或服务参数的虚假陈述，则会失去相关 TI 产品或服务的明示或暗示保证，且构成不公平的、欺诈性商业行为。TI 对此类虚假陈述不承担任何责任。

买方和在系统中整合 TI 产品的其他开发人员（总称“设计人员”）理解并同意，设计人员在设计应用时应自行实施独立的分析、评价和判断，且应全权负责并确保应用的安全性，及设计人员的应用（包括应用中使用的 TI 产品）应符合所有适用的法律法规及其他相关要求。设计人员就自己设计的应用声明，其具备制订和实施下列保障措施所需的一切必要专业知识，能够 (1) 预见故障的危险后果，(2) 监视故障及其后果，以及 (3) 降低可能导致危险的故障几率并采取适当措施。设计人员同意，在使用或分发包含 TI 产品的任何应用前，将彻底测试该等应用和该等应用中所用 TI 产品的功能。

TI 提供技术、应用或其他设计建议、质量特点、可靠性数据或其他服务或信息，包括但不限于与评估模块有关的参考设计和材料（总称“TI 资源”），旨在帮助设计人员开发整合了 TI 产品的应用，如果设计人员（个人，或如果是代表公司，则为设计人员的公司）以任何方式下载、访问或使用任何特定的 TI 资源，即表示其同意仅为该等目标，按照本通知的条款使用任何特定 TI 资源。

TI 所提供的 TI 资源，并未扩大或以其他方式修改 TI 对 TI 产品的公开适用的质保及质保免责声明；也未导致 TI 承担任何额外的义务或责任。TI 有权对其 TI 资源进行纠正、增强、改进和其他修改。除特定 TI 资源的公开文档中明确列出的测试外，TI 未进行任何其他测试。

设计人员只有在开发包含该等 TI 资源所列 TI 产品的应用时，才被授权使用、复制和修改任何相关 TI 资源。但并未依据禁止反言原则或其他法律授予您任何 TI 知识产权的任何其他明示或默示的许可，也未授予您 TI 或第三方的任何技术或知识产权的许可，该等许可包括但不限于任何专利权、版权、屏蔽作品权或与其他 TI 产品或服务的任何整合、机器制作、流程相关的其他知识产权。涉及或参考了第三方产品或服务的信息不构成使用此类产品或服务的许可或与其相关的保证或认可。使用 TI 资源可能需要您向第三方获得对该等第三方专利或其他知识产权的许可。

TI 资源系“按原样”提供。TI 兹免除对资源及其使用作出所有其他明确或默示的保证或陈述，包括但不限于对准确性或完整性、产权保证、无屡发故障保证，以及适销性、适合特定用途和不侵犯任何第三方知识产权的任何默认保证。TI 不负任何责任，包括但不限于因组合产品所致或与之有关的申索，也不为或对设计人员进行辩护或赔偿，即使该等产品组合已列于 TI 资源或其他地方。对因 TI 资源或其使用引起或与之有关的任何实际的、直接的、特殊的、附带的、间接的、惩罚性的、偶发的、从属或惩戒性损害赔偿，不管 TI 是否获悉可能会产生上述损害赔偿，TI 概不负责。

除 TI 已明确指出特定产品已达到特定行业标准（例如 ISO/TS 16949 和 ISO 26262）的要求外，TI 不对未达到任何该等行业标准要求而承担任何责任。

如果 TI 明确宣称产品有助于功能安全或符合行业功能安全标准，则该等产品旨在帮助客户设计和创作自己的符合相关功能安全标准和要求的的应用。在应用内使用产品的行为本身不会配有安全特性。设计人员必须确保遵守适用于其应用的相关安全要求和标准。设计人员不可将任何 TI 产品用于关乎性命的医疗设备，除非已由各方获得授权的管理人员签署专门的合同对此类应用专门作出规定。关乎性命的医疗设备是指出现故障会导致严重身体伤害或死亡的医疗设备（例如生命保障设备、心脏起搏器、心脏除颤器、人工心脏泵、神经刺激器以及植入设备）。此类设备包括但不限于，美国食品药品监督管理局认定为 III 类设备的设备，以及在美国以外的其他国家或地区认定为同等类别设备的所有医疗设备。

TI 可能明确指定某些产品具备某些特定资格（例如 Q100、军用级或增强型产品）。设计人员同意，其具备一切必要专业知识，可以为自己的应用选择适合的产品，并且正确选择产品的风险由设计人员承担。设计人员单方面负责遵守与该等选择有关的所有法律或监管要求。

设计人员同意向 TI 及其代表全额赔偿因其不遵守本通知条款和条件而引起的任何损害、费用、损失和/或责任。

邮寄地址：上海市浦东新区世纪大道 1568 号中建大厦 32 楼，邮政编码：200122
Copyright © 2017 德州仪器半导体技术（上海）有限公司