

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED

Prepared in accordance with ASME Y14.24

Vendor item drawing

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PMIC N/A	PREPARED BY RICK OFFICER		DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990 http://www.landandmaritime.dla.mil/	
Original date of drawing YY-MM-DD 13-05-29	CHECKED BY RAJESH PITHADIA		TITLE MICROCIRCUIT, LINEAR, LOW NOISE, HIGH SPEED DIFFERENTIAL AMPLIFIER, MONOLITHIC SILICON	
	APPROVED BY CHARLES F. SAFFLE			
	SIZE A	CODE IDENT. NO. 16236	DWG NO. V62/13615	
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1. SCOPE

1.1 Scope. This drawing documents the general requirements of a high performance low noise, high speed differential amplifier microcircuit, with an operating temperature range of -55°C to +105°C.

1.2 Vendor Item Drawing Administrative Control Number. The manufacturer's PIN is the item of identification. The vendor item drawing establishes an administrative control number for identifying the item on the engineering documentation:

<u>V62/13615</u>	-	<u>01</u>	<u>X</u>	<u>E</u>
Drawing number		Device type (See 1.2.1)	Case outline (See 1.2.2)	Lead finish (See 1.2.3)

1.2.1 Device type(s).

<u>Device type</u>	<u>Generic</u>	<u>Circuit function</u>
01	ADA4930-1-EP	Low noise, high speed differential amplifier

1.2.2 Case outline(s). The case outline(s) are as specified herein.

<u>Outline letter</u>	<u>Number of pins</u>	<u>JEDEC PUB 95</u>	<u>Package style</u>
X	16	MO-220-WEED	Lead frame chip scale

1.2.3 Lead finishes. The lead finishes are as specified below or other lead finishes as provided by the device manufacturer:

<u>Finish designator</u>	<u>Material</u>
A	Hot solder dip
B	Tin-lead plate
C	Gold plate
D	Palladium
E	Gold flash palladium
Z	Other

1.3 Absolute maximum ratings. 1/

Supply voltage range (V _S)	5.5 V
Power dissipation (P _D) at T _A = +105°C	550 mW
Junction temperature range (T _J)	150°C
Storage temperature range (T _{STG})	-65°C to +125°C
Lead temperature (soldering, 10 seconds)	300°C

1.4 Recommended operating conditions. 2/

Supply voltage range (V _S)	3.3 V to 5 V
Operating free-air temperature range (T _A)	-55°C to +105°C

1/ Stresses beyond those listed under "absolute maximum rating" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

2/ Use of this product beyond the manufacturers design rules or stated parameters is done at the user's risk. The manufacturer and/or distributor maintain no responsibility or liability for product used beyond the stated limits.

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1.5 Thermal characteristics.

Thermal resistance, junction to ambient (θ_{JA}) 81.6°C/W

2. APPLICABLE DOCUMENTS

JEDEC Solid State Technology Association

JEDEC PUB 95 – Registered and Standard Outlines for Semiconductor Devices

(Applications for copies should be addressed to the Electronic Industries Alliance, 2500 Wilson Boulevard, Arlington, VA 22201-3834 or online at <http://www.jedec.org>)

3. REQUIREMENTS

3.1 Marking. Parts shall be permanently and legibly marked with the manufacturer's part number as shown in 6.3 herein and as follows:

- A. Manufacturer's name, CAGE code, or logo
- B. Pin 1 identifier and H2X
- C. ESDS identification (optional)

3.2 Unit container. The unit container shall be marked with the manufacturer's part number and with items A and C (if applicable) above.

3.3 Electrical characteristics. The maximum and recommended operating conditions and electrical performance characteristics are as specified in 1.3, 1.4, and table I herein.

3.4 Design, construction, and physical dimension. The design, construction, and physical dimensions are as specified herein.

3.5 Diagrams.

3.5.1 Case outline. The case outline shall be as shown in 1.2.2 and figure 1.

3.5.2 Terminal connections. The terminal connections shall be as shown in figure 2.

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TABLE I. Electrical performance characteristics. 1/

Test	Symbol	Conditions <u>2/</u> V _S = 3.3 V, unless otherwise specified	Temperature, T _A	Device type	Limits		Unit
					Min	Max	
Dynamic performance.							
-3 dB small signal bandwidth	SSBW	V _{O,dm} = 0.1 V _{P-P}	+25°C	01	1430 typical	MHz	
-3 dB large signal bandwidth	LSBW	V _{O,dm} = 2 V _{P-P}	+25°C	01	887 typical	MHz	
Bandwidth for 0.1 dB flatness		V _{O,dm} = 0.1 V _{P-P}	+25°C	01	380 typical	MHz	
Slew rate	SR	V _{O,dm} = 2 V step, 25% to 75%	+25°C	01	2877 typical	V/μs	
Settling time to 0.1%	t _S	V _{O,dm} = 2 V step, R _L = 200 Ω	+25°C	01	6.3 typical	ns	
Overdrive recovery time		G = 3, V _{IN,dm} = 0.7 V _{P-P} pulse	+25°C	01	1.5 typical	ns	
Noise / harmonic performance.							
Second harmonic distortion	HD2	V _{O,dm} = 2 V _{P-P} , f _C = 10 MHz	-55°C to +105°C	01	-98 typical	dBc	
		V _{O,dm} = 2 V _{P-P} , f _C = 30 MHz			-91 typical		
		V _{O,dm} = 2 V _{P-P} , f _C = 70 MHz			-79 typical		
		V _{O,dm} = 2 V _{P-P} , f _C = 100 MHz			-73 typical		
Third harmonic distortion	HD3	V _{O,dm} = 2 V _{P-P} , f _C = 10 MHz	-55°C to +105°C	01	-97 typical	dBc	
		V _{O,dm} = 2 V _{P-P} , f _C = 30 MHz			-88 typical		
		V _{O,dm} = 2 V _{P-P} , f _C = 70 MHz			-79 typical		
		V _{O,dm} = 2 V _{P-P} , f _C = 100 MHz			-73 typical		
Third order intermodulation distortion (IMD)		V _{O,dm} = 1 V _{P-P} / tone, f _C = 70.05 MHz ±0.05 MHz	+25°C	01	91 typical	dBc	
		V _{O,dm} = 1 V _{P-P} / tone, f _C = 140.05 MHz ±0.05 MHz			86 typical		

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued. 1/

Test	Symbol	Conditions <u>2/</u> V _S = 3.3 V, unless otherwise specified	Temperature, T _A	Device type	Limits		Unit
					Min	Max	
Noise / harmonic performance - continued.							
Input voltage noise		f = 100 kHz	+25°C	01	1.15 typical		nV /
			-55°C to +105°C		1.2 typical		√Hz
Input current noise		f = 100 kHz	+25°C	01	3 typical		pA / √Hz
DC performance.							
Input offset voltage	V _{IO}	V _{IP} = V _{IN} = V _{OCM} = 0 V, R _L = open circuit	-55°C to +105°C	01	-3.1	+3.1	mV
Input offset voltage drift	ΔV _{IO}		-55°C to +105°C	01	2.75 typical		μV / °C
Input bias current	I _{IB}		+25°C	01	-36	-16	μA
Input bias current drift	ΔI _{IB}		-55°C to +105°C	01	-0.05 typical		μA / °C
Input offset current	I _{IO}		+25°C	01	-1.8	+1.8	μA
Open loop gain		R _F = R _G = 10 kΩ, ΔV _O = 0.5 V, R _L = open circuit	+25°C	01	64 typical		dB
			-55°C to +105°C		61 typical		
Input characteristics.							
Input common mode voltage range			+25°C	01	0.3	1.2	V
Input resistance	R _{IN}	Differential	+25°C	01	150 typical		kΩ
		Common mode			3 typical		MΩ

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued. 1/

Test	Symbol	Conditions <u>2/</u> V _S = 3.3 V, unless otherwise specified	Temperature, T _A	Device type	Limits		Unit
					Min	Max	
Input characteristics – continued.							
Input capacitance	C _{IN}	Common mode	+25°C	01	1 typical		pF
Common mode rejection mode	CMRR	ΔV _{ICM} = 0.5 V dc;	+25°C	01		-77	dB
		R _F = R _G = 10 kΩ, R _L = open circuit	-55°C to +105°C		-76 typical		
Output characteristics.							
Output voltage	V _{OUT}	Each single ended output, R _F = R _G = 10 kΩ	+25°C	01	0.11	1.74	V
			-55°C to +105°C			1.54	
Linear output current		Each single ended output, f = 1 MHz, THD ≤ 60 dBc	+25°C	01	30 typical		mA
Output balance error		f = 1 MHz	+25°C	01	55 typical		dB
3.3 V V _{OCM} to V _{O,CM} performance.							
V _{OCM} dynamic performance.							
-3 dB bandwidth		V _{O,CM} = 0.1 V _{P-P}	+25°C	01	745 typical		MHz
Slew rate	SR	V _{O,CM} = 2 V _{P-P} , 25% to 75%	+25°C	01	828 typical		V/μs
V _{OCM} input characteristics.							
Input voltage range	V _{IN}		+25°C	01	0.8	1.1	V
Input resistance	R _{IN}		+25°C	01	7.0	10.3	kΩ
Input offset voltage	V _{IO}	V _{OS,CM} = V _{O,CM} – V _{OCM} ; V _{IP} = V _{IN} = V _{OCM} = 0 V	+25°C	01	-25	+31	mV

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued. 1/

Test	Symbol	Conditions <u>2/</u> V _S = 3.3 V, unless otherwise specified	Temperature, T _A	Device type	Limits		Unit
					Min	Max	
3.3 V V _{OCM} to V _{O,CM} performance – continued.							
V _{OCM} input characteristics continued.							
Input voltage noise		f = 100 kHz	+25°C	01	23.5 typical		nV / √Hz
Gain			+25°C	01	0.99	1.02	V/V
			-55°C to +105°C		1.01 typical		
Common mode rejection ratio		ΔV _{ICM} = 0.5 V dc; R _F = R _G = 10 kΩ, R _L = open circuit	+25°C	01		-77	dB
			-55°C to +105°C		-76 typical		
3.3 V general performance.							
Power supply.							
Operating range			+25°C	01	3.3 typical		V
Quiescent current per amplifier		Enabled	+25°C	01	32	40	mA
		Enabled variation	-55°C to +105°C		81 typical		μA/ °C
		Disabled	+25°C		0.44	2.35	mA
			-55°C to +105°C		2.4 typical		
Power supply rejection ratio	+PSRR	ΔV _{ICM} = 0.5 V dc; R _F = R _G = 10 kΩ, R _L = open circuit	+25°C	01		-70	dB
			-55°C to +105°C		-68 typical		
	-PSRR	ΔV _{ICM} = 0.5 V dc; R _F = R _G = 10 kΩ, R _L = open circuit	+25°C			-76	
			-55°C to +105°C		-77 typical		

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued. 1/

Test	Symbol	Conditions <u>2/</u> V _S = 3.3 V, unless otherwise specified	Temperature, T _A	Device type	Limits		Unit
					Min	Max	
3.3 V general performance - continued.							
Power down ($\overline{\text{PD}}$).							
$\overline{\text{PD}}$ input voltage		Disabled	+25°C	01	< 0.8 typical		V
		Enabled			>1.3 typical		
Turn off time			+25°C	01	1 typical		μs
Turn on time			+25°C	01	12 typical		ns
$\overline{\text{PD}}$ pin bias current		Enabled, $\overline{\text{PD}}$ = 3.3 V	+25°C	01	0.09 typical		μA
		Disabled, $\overline{\text{PD}}$ = 0 V			97 typical		
Operating temperature range				01	-55	+105	°C

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued. 1/

Test	Symbol	Conditions <u>2/</u> V _S = 5 V, unless otherwise specified	Temperature, T _A	Device type	Limits		Unit
					Min	Max	
Dynamic performance.							
-3 dB small signal bandwidth	SSBW	V _{O,dm} = 0.1 V _{P-P}	+25°C	01	1350 typical	MHz	
-3 dB large signal bandwidth	LSBW	V _{O,dm} = 2 V _{P-P}	+25°C	01	937 typical	MHz	
Bandwidth for 0.1 dB flatness		V _{O,dm} = 0.1 V _{P-P}	+25°C	01	369 typical	MHz	
Slew rate	SR	V _{O,dm} = 2 V step, 25% to 75%	+25°C	01	3400 typical	V/μs	
Settling time to 0.1%	t _S	V _{O,dm} = 2 V step, R _L = 200 Ω	+25°C	01	6 typical	ns	
Overdrive recovery time		G = 3, V _{IN,dm} = 0.7 V _{P-P} pulse	+25°C	01	1.5 typical	ns	
Noise / harmonic performance.							
Second harmonic distortion	HD2	V _{O,dm} = 2 V _{P-P} , f _C = 10 MHz	-55°C to +105°C	01	-104 typical	dBc	
		V _{O,dm} = 2 V _{P-P} , f _C = 30 MHz			-91 typical		
		V _{O,dm} = 2 V _{P-P} , f _C = 70 MHz			-79 typical		
		V _{O,dm} = 2 V _{P-P} , f _C = 100 MHz			-73 typical		
Third harmonic distortion	HD3	V _{O,dm} = 2 V _{P-P} , f _C = 10 MHz	-55°C to +105°C	01	-101 typical	dBc	
		V _{O,dm} = 2 V _{P-P} , f _C = 30 MHz			-93 typical		
		V _{O,dm} = 2 V _{P-P} , f _C = 70 MHz			-82 typical		
		V _{O,dm} = 2 V _{P-P} , f _C = 100 MHz			-75 typical		
Third order intermodulation distortion (IMD)		V _{O,dm} = 1 V _{P-P} / tone, f _C = 70.05 MHz ±0.05 MHz	+25°C	01	94 typical	dBc	
		V _{O,dm} = 1 V _{P-P} / tone, f _C = 140.05 MHz ±0.05 MHz			90 typical		

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued. 1/

Test	Symbol	Conditions <u>2/</u> V _S = 5 V, unless otherwise specified	Temperature, T _A	Device type	Limits		Unit
					Min	Max	
Noise / harmonic performance - continued.							
Input voltage noise		f = 100 kHz	+25°C	01	1.2 typical		nV /
			-55°C to +105°C		1.3 typical		√Hz
Input current noise		f = 100 kHz	+25°C	01	2.8 typical		pA / √Hz
DC performance.							
Input offset voltage	V _{IO}	V _{IP} = V _{IN} = V _{OCM} = 0 V, R _L = open circuit	-55°C to +105°C	01	-3.1	+3.1	mV
Input offset voltage drift	ΔV _{IO}		-55°C to +105°C	01	1.8 typical		μV / °C
Input bias current	I _{IB}		+25°C	01	-34	-15	μA
Input bias current drift	ΔI _{IB}		-55°C to +105°C	01	-0.05 typical		μA / °C
Input offset current	I _{IO}		+25°C	01	-0.82	+0.82	μA
Open loop gain		R _F = R _G = 10 kΩ, ΔV _O = 1 V, R _L = open circuit	+25°C	01	64 typical		dB
			-55°C to +105°C		61 typical		
Input characteristics.							
Input common mode voltage range			+25°C	01	0.3	2.8	V
Input resistance	R _{IN}	Differential	+25°C	01	150 typical		kΩ
		Common mode			3 typical		MΩ

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued. 1/

Test	Symbol	Conditions <u>2/</u> V _S = 5 V, unless otherwise specified	Temperature, T _A	Device type	Limits		Unit
					Min	Max	
Input characteristics – continued.							
Input capacitance	C _{IN}	Common mode	+25°C	01	1 typical		pF
Common mode rejection mode	CMRR	ΔV _{ICM} = 1 V dc; R _F = R _G = 10 kΩ, R _L = open circuit	+25°C	01		-77	dB
			-55°C to +105°C		-76 typical		
Output characteristics.							
Output voltage	V _{OUT}	Each single ended output, R _F = R _G = 10 kΩ	-55°C to +105°C	01	0.18	3.38	V
Linear output current		Each single ended output, f = 1 MHz, THD ≤ 60 dBc	+25°C	01	30 typical		mA
Output balance error		f = 1 MHz	+25°C	01	55 typical		dB
5 V V _{OCM} to V _{O,CM} performance.							
V _{OCM} dynamic performance.							
-3 dB bandwidth		V _{O,CM} = 0.1 V _{P-P}	+25°C	01	740 typical		MHz
Slew rate	SR	V _{O,CM} = 2 V _{P-P} , 25% to 75%	+25°C	01	1224 typical		V/μs
V _{OCM} input characteristics.							
Input voltage range	V _{IN}		+25°C	01	0.5	2.3	V
Input resistance	R _{IN}		+25°C	01	7.0	10.2	kΩ
Input offset voltage	V _{IO}	V _{OS,CM} = V _{O,CM} – V _{OCM} ; V _{IP} = V _{IN} = V _{OCM} = 0 V	+25°C	01	-25	+15	mV

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued. 1/

Test	Symbol	Conditions <u>2/</u> V _S = 5 V, unless otherwise specified	Temperature, T _A	Device type	Limits		Unit
					Min	Max	
5 V V _{OCM} to V _{O,CM} performance – continued.							
V _{OCM} input characteristics - continued.							
Input voltage noise		f = 100 kHz	+25°C	01	23.5 typical		nV / √Hz
Gain			+25°C	01	0.99	1.02	V/V
			-55°C to +105°C		1 typical		
Common mode rejection ratio		ΔV _{OCM} = 1.5 V dc; R _F = R _G = 10 kΩ, R _L = open circuit	+25°C	01		-77	dB
			-55°C to +105°C		-76 typical		
5 V general performance.							
Power supply.							
Operating range			+25°C	01	5 typical		V
Quiescent current per amplifier		Enabled	+25°C	01	31.1	38.4	mA
		Enabled variation	-55°C to +105°C		74.5 typical		μA/ °C
		Disabled	+25°C		0.45	2.6	mA
			-55°C to +105°C		2.7 typical		
Power supply rejection ratio	+PSRR	ΔV _{ICM} = 1 V dc; R _F = R _G = 10 kΩ, R _L = open circuit	+25°C	01		-71	dB
			-55°C to +105°C		-70 typical		
	-PSRR	ΔV _{ICM} = 1 V dc; R _F = R _G = 10 kΩ, R _L = open circuit	+25°C			-75	
			-55°C to +105°C		-78 typical		

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued. 1/

Test	Symbol	Conditions <u>2/</u> V _S = 5 V, unless otherwise specified	Temperature, T _A	Device type	Limits		Unit
					Min	Max	
5 V general performance.							
Power down ($\overline{\text{PD}}$).							
$\overline{\text{PD}}$ input voltage		Disabled	+25°C	01	< 2.5 typical		V
		Enabled			>3 typical		
Turn off time			+25°C	01	1 typical		μs
Turn on time			+25°C	01	12 typical		ns
$\overline{\text{PD}}$ pin bias current		Enabled, $\overline{\text{PD}}$ = 5 V	+25°C	01	0.09 typical		μA
		Disabled, $\overline{\text{PD}}$ = 0 V			97 typical		
Operating temperature range				01	-55	+105	°C

1/ Testing and other quality control techniques are used to the extent deemed necessary to assure product performance over the specified temperature range. Product may not necessarily be tested across the full temperature range and all parameters may not necessarily be tested. In the absence of specific parametric testing, product performance is assured by characterization and/or design.

2/ Unless otherwise specified, $V_{\text{ICM}} = 0.9\text{ V}$, $V_{\text{OCM}} = 0.9\text{ V}$, fixed resistance (R_F) = 301 Ω, gain resistance (R_G) = 301 Ω, load resistance ($R_{\text{L, dm}}$) = 1 kΩ, single ended input, differential output.

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Case X

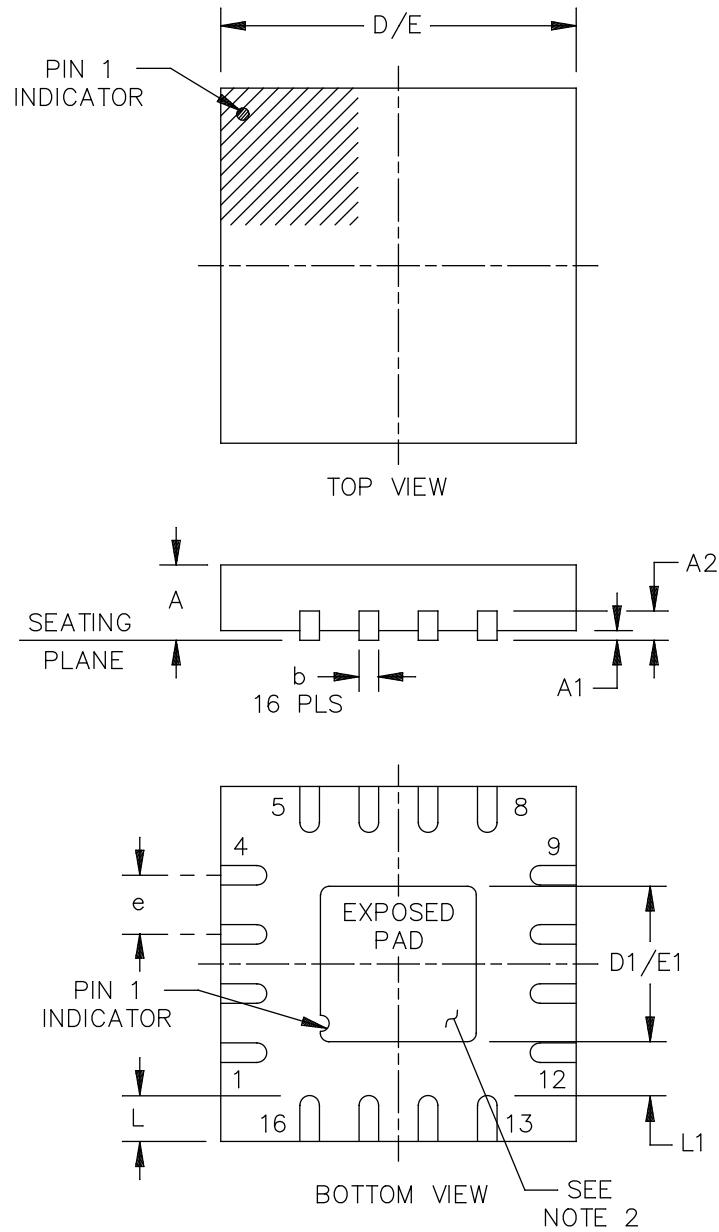


FIGURE 1. Case outline.

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Case X – continued.

Symbol	Dimensions					
	Inches			Millimeters		
	Min	Med	Max	Min	Med	Max
A	0.027	0.029	0.031	0.70	0.75	0.80
A1	0.0007	---	0.001	0.02	---	0.05
A2	0.007 REF			0.20 REF		
b	0.007	0.009	0.011	0.18	0.23	0.30
D/E	0.114	0.118 SQ	0.122	2.90	3.00 SQ	3.10
D1/E1	0.045	0.051 SQ	0.057	1.15	1.30 SQ	1.45
e	0.019 BSC			0.50 BSC		
L	0.011	0.015	0.019	0.30	0.40	0.50
L1	0.009	---	---	0.25	---	---

NOTES:

1. Controlling dimensions are millimeter, inch dimensions are given for reference only.
2. The exposed pad should be connected to a thermal ground to dissipate heat. For proper connection of the exposed pad, refer to the pin configuration and function descriptions section of the manufacturer's datasheet.
3. Falls within reference to JEDEC MO-220-WEED.

FIGURE 1. Case outline - Continued.

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Device type	01	
Case outline	X	
Terminal number	Terminal symbol	Description
1	-FB	Negative output for feedback component connection
2	+IN	Positive input summing node.
3	-IN	Negative input summing node.
4	+FB	Positive output for feedback component connection
5	+V _S	Positive supply voltage.
6	+V _S	Positive supply voltage.
7	+V _S	Positive supply voltage.
8	+V _S	Positive supply voltage.
9	V _{OCM}	Output common mode voltage.
10	+OUT	Positive output for load connection.
11	-OUT	Negative output for load connection.
12	$\overline{\text{PD}}$	Power down pin.
13	-V _S	Negative supply voltage.
14	-V _S	Negative supply voltage.
15	-V _S	Negative supply voltage.
16	-V _S	Negative supply voltage.
	EPAD	Exposed pad. The exposed pad is not electrically connected to the device. It is typically soldered to ground or a power plane on the printed circuit board (PCB) that is thermally conductive.

FIGURE 2. Terminal connections.

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4. VERIFICATION

4.1 Product assurance requirements. The manufacturer is responsible for performing all inspection and test requirements as indicated in their internal documentation. Such procedures should include proper handling of electrostatic sensitive devices, classification, packaging, and labeling of moisture sensitive devices, as applicable.

5. PREPARATION FOR DELIVERY

5.1 Packaging. Preservation, packaging, labeling, and marking shall be in accordance with the manufacturer's standard commercial practices for electrostatic discharge sensitive devices.

6. NOTES

6.1 ESDS. Devices are electrostatic discharge sensitive and are classified as ESDS class 1 minimum.

6.2 Configuration control. The data contained herein is based on the salient characteristics of the device manufacturer's data book. The device manufacturer reserves the right to make changes without notice. This drawing will be modified as changes are provided.

6.3 Suggested source(s) of supply. Identification of the suggested source(s) of supply herein is not to be construed as a guarantee of present or continued availability as a source of supply for the item. DLA Land and Maritime maintains an online database of all current sources of supply at <http://www.landandmaritime.dla.mil/Programs/Smcr/>.

Vendor item drawing administrative control number <u>1/</u>	Device manufacturer CAGE code	Transportation mode and order quantity	Vendor part number <u>2/</u>
V62/13615-01XE	24355	Reel, 250	ADA4930-1SCPZ-EPR2
		Reel, 1,500	ADA4930-1SCPZ-EPR7
		Reel, 5,000	ADA4930-1SCPZ-EPRL

1/ The vendor item drawing establishes an administrative control number for identifying the item on the engineering documentation.

2/ Due to size limitation, the vendor will mark the part with "H2X" in lieu of the vendor part number.

CAGE code

24355

Source of supply

Analog Devices
Route 1 Industrial Park
P.O. Box 9106
Norwood, MA 02062
Point of contact: Raheen Business Park
Limerick, Ireland

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