

SOT-23 Encapsulate Adjustable Reference Source

CJ432 Adjustable Accurate Reference Source

DEVICE DESCRIPTION

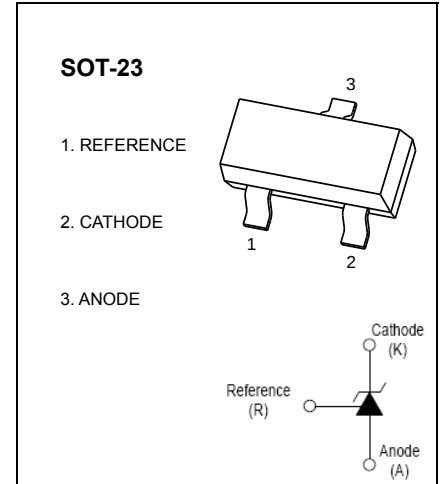
The CJ432 is a three-terminal Shunt Voltage Reference providing a highly accurate 1.24V. The CJ432 thermal stability and wide operating current, makes it suitable for all variety of applications that are looking for a low cost solution with high performance.

FEATURES

- Low dynamic output impedance
- The effective temperature compensation in the working range of full temperature
- Low output noise voltage
- Fast on-state response
- Sink current capability of 0.1mA to 100mA

APPLICATION

- Shunt Regulator
- High-Current Shunt Regulator
- Precision Current Limiter



ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

Parameter	Symbol	Value	Units
Cathode Voltage	V_{KA}	18	V
Cathode Current Range (continuous)	I_{KA}	100	mA
Reference Input Current Range	I_{ref}	6	μA
Power Dissipation	P_D	350	mW
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	357	$^{\circ}C/W$
Operating Temperature	T_{opr}	0~+70	$^{\circ}C$
Junction Temperature	T_J	150	$^{\circ}C$
Storage Temperature	T_{stg}	-65~+150	$^{\circ}C$

ELECTRICAL CHARACTERISTICS (T_a=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Reference input voltage (Fig 1)	V _{ref}	V _{KA} =V _{REF} , I _{KA} =10mA	1.2214		1.2586	V
Deviation of reference voltage over full temperature range (Fig 1)	ΔV _{ref(DEV)}	V _{KA} =V _{REF} , I _{KA} =10mA 0°C≤T _a ≤70°C			16	mV
Ratio of change in reference input voltage to the change in cathode voltage (Fig 2)	ΔV _{ref} /ΔV _{KA}	I _{KA} =10mA, ΔV _{KA} =1.25V~15V			2.4	mV/V
Deviation of reference input current over full temperature range (Fig 2)	ΔI _{ref} /ΔT	I _{KA} =10mA, R ₁ =10kΩ, R ₂ =∞, 0°C≤T _a ≤70°C			0.6	μA
Minimum cathode current for regulation (Fig 1)	I _{KA(min)}	V _{KA} =V _{REF}			0.1	mA
Off-state cathode current(Fig 3)	I _{off}	V _{KA} =15V, V _{REF} =0			0.5	μA
Dynamic impedance	Z _{KA}	V _{KA} =V _{REF} , I _{KA} =0.1 ~20mA, f≤1.0kHz			0.5	Ω

CLASSIFICATION OF V_{ref}

Rank	1%	1.5%
Range	1.2276~1.2524	1.2214~1.2586

Figure 1. Test Circuit for V_{KA} = V_{ref}

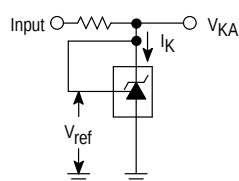


Figure 2. Test Circuit for V_{KA} > V_{ref}

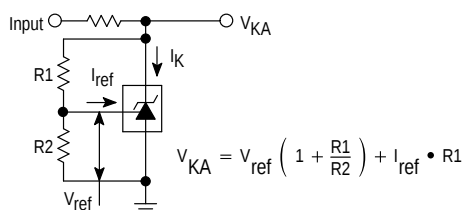
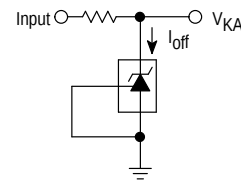
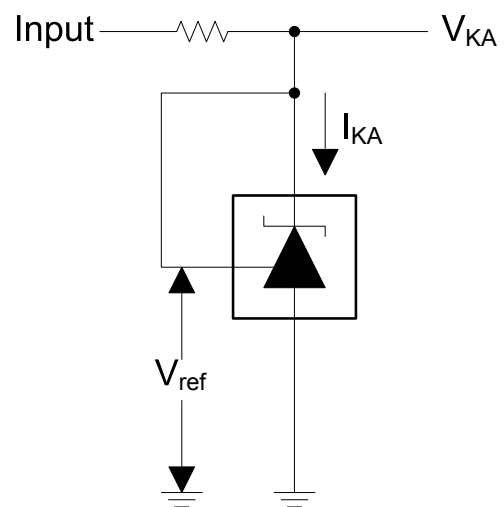
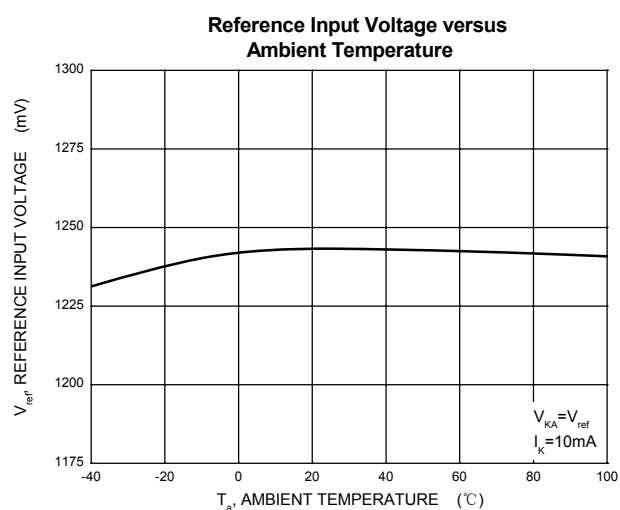
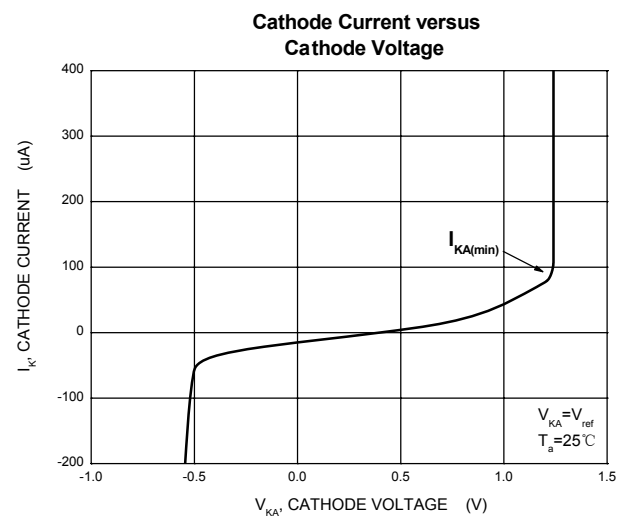
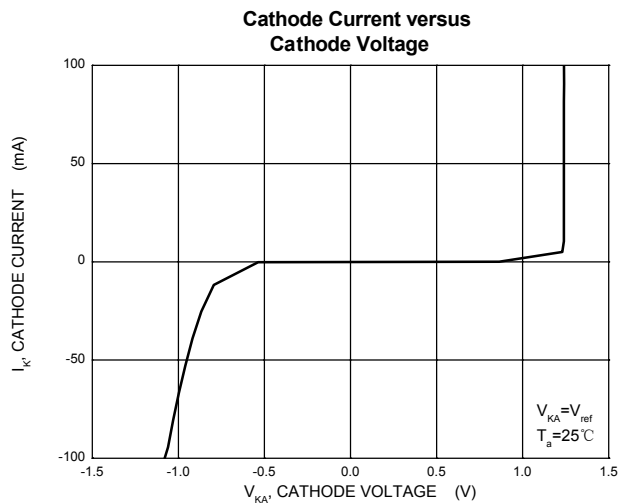


Figure 3. Test Circuit for I_{off}

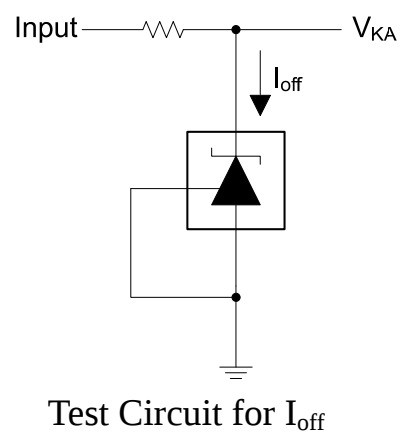
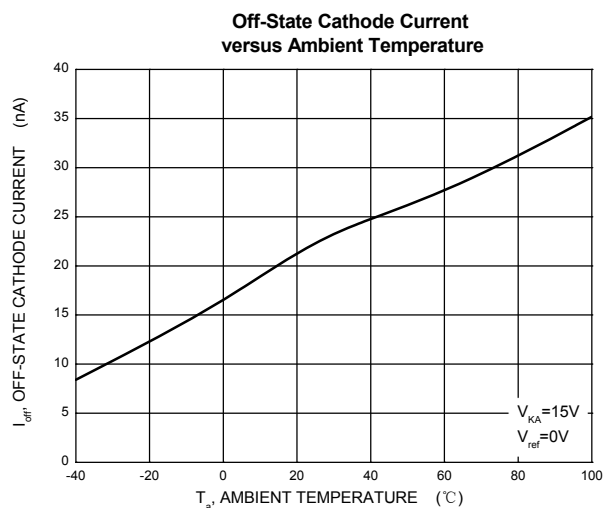
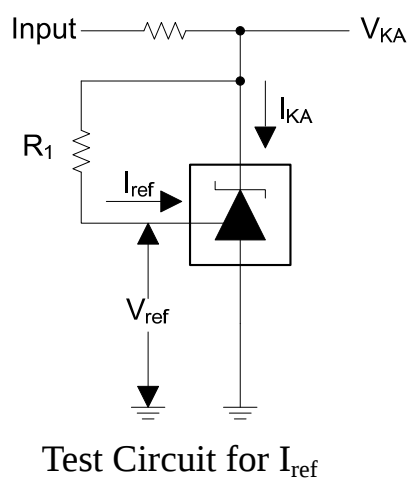
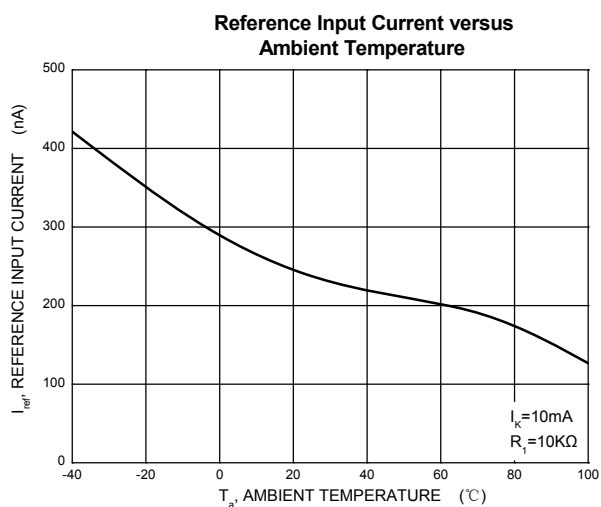
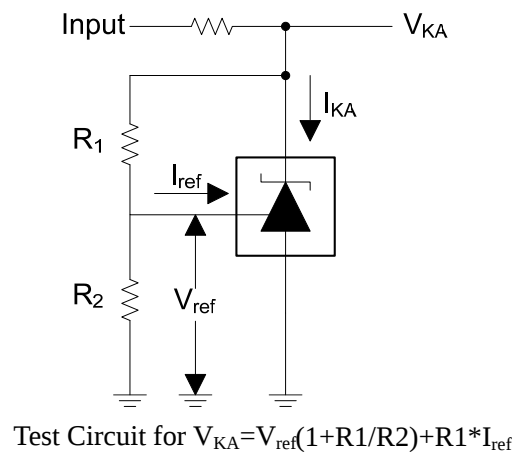
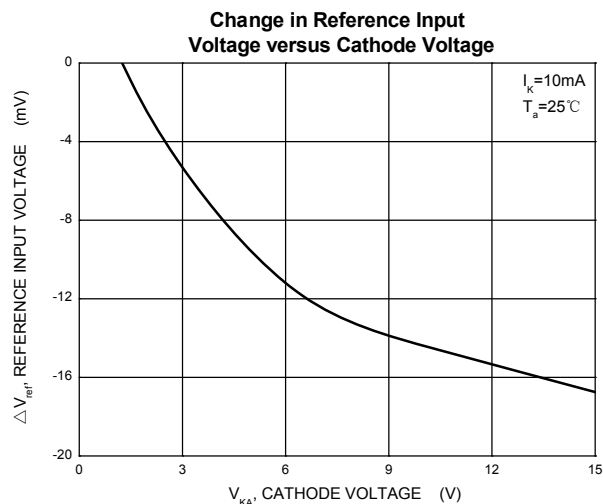


Typical Characteristics

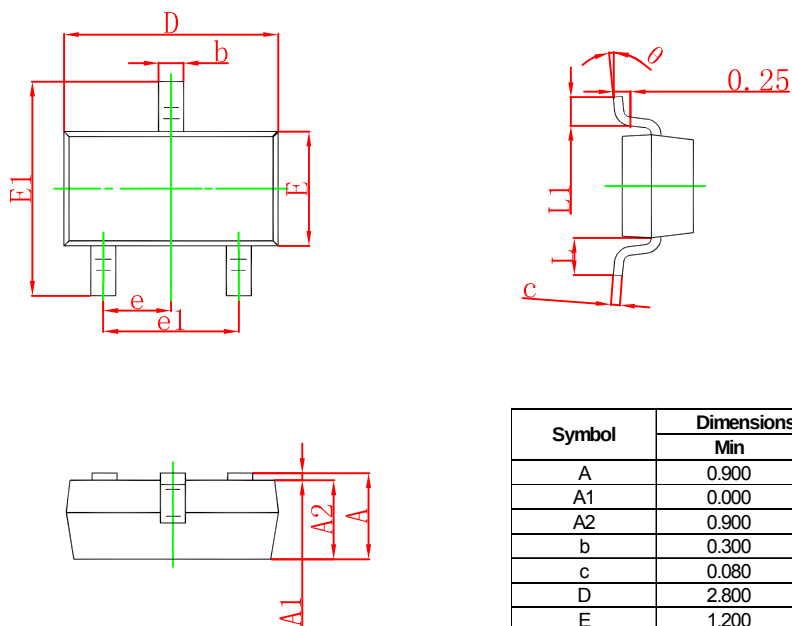


Test Circuit for $V_{KA} = V_{ref}$

Typical Characteristics

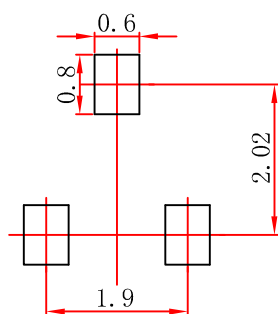


SOT-23 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

SOT-23 Suggested Pad Layout



- Note:
1. Controlling dimension: in millimeters.
 2. General tolerance: ± 0.05 mm.
 3. The pad layout is for reference purposes only.

NOTICE

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