



PCB Layout(Pattern Side)

Memory Card Pin Assignment	DESCRIPTION
Pin No.	NAME
1	DATE 2 I/O/PP Date Line(bit2)
2	CD/DAT 3 I/O/PP Card Detect/Date Line(bit3)
3	CMD PP Command Response
4	VDD S Supply Voltage
5	CLK I Clock
6	VSS S Supply Voltage Ground
7	DAT0 I/O/PP Date Line(bit0)
8	DAT1 I/O/PP Date Line(bit1)

Specification

Electrical Characteristics:
 Contact Current Rating: 0.5 Amperes.
 Dielectric Withstanding Voltage: AC500V r.m.s.
 Insulation Resistance: 1000 MΩ Minimum.
 Contact Resistance: 40 mΩ Maximum.
 Environmental:
 Operating Temperature: -25°C~+85°C.
 Environmental:
 Mating Cycles: 10,000 Insertions.

③	Shell	—	1	SUS	Ni Plated	—
②	Contact	—	8	Phosphor Bronze	Ag Plated	—
①	Housing	—	1	LCP	Black	—
ITEM	PART NAME	TER'NO.	QTY.	MATERIAL	FINISHING	REMARK

APPROVALS		DATE	TITLE:
DRAWN	JKUN.LU	2014/9/29	SD
ELECTRONICS CO.,LTD			

ECN NO.	REV.	DATE.	DESCRIPTION.	CHANGE.	CHECK.	APPRO.
	A		NEW			
APPROVALS			TITLE:	PART NO.	SCALE:	PROJ.:
CHECKED			DATE:	TF-04A	1:1	PROJ.:
APPROVALS			DATE:			PROJ.:
TOLERANCES ARE			±0.30	±0.25	±0.20	±0.15
			30~12.5	10~30	5~10	~5
			±2'			
			DRAWING NO.			SHEET 1 OF 1